Changes for the Better



MELSEC PROCESS CONTROL/REDUNDANT SYSTEM



MELSEC process control is a flexible, highly reliable platform with advanced functionality designed to cost-effectively meet the needs of a wide range of industries.

Reduce costs

Mitsubishi Electric automation products give users the flexibility to configure their systems according to their needs, unlike a distributed control system (DCS), which is developed by the vender from the initial design phase. This can greatly reduce initial and running costs.

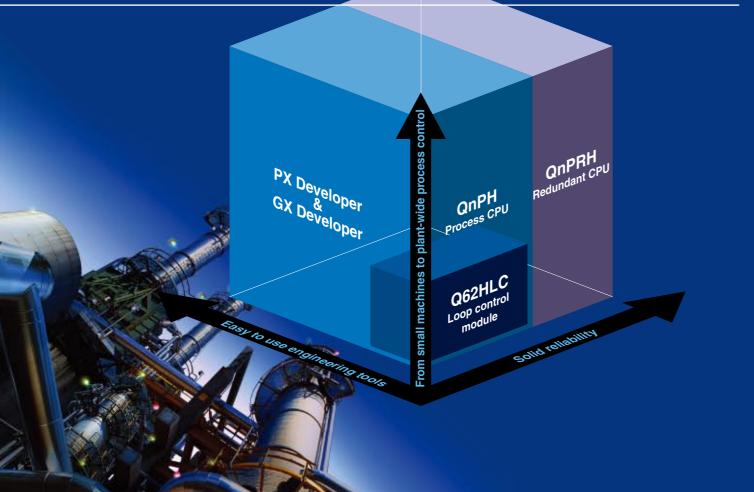
Implement sophisticated process control

Designed to handle large numbers of proportional, integral, and derivative operations (PID loops), Mitsubishi process and redundant CPUs are well suited for demanding control applications.

Maximize system availability

Increase reliability and prevent important processes from being interrupted by using redundant CPUs and hardware. In the event of a failure, modules can be hot swapped.

MELSEC PROCESS CONTROL/REDUNDANT SYSTEM









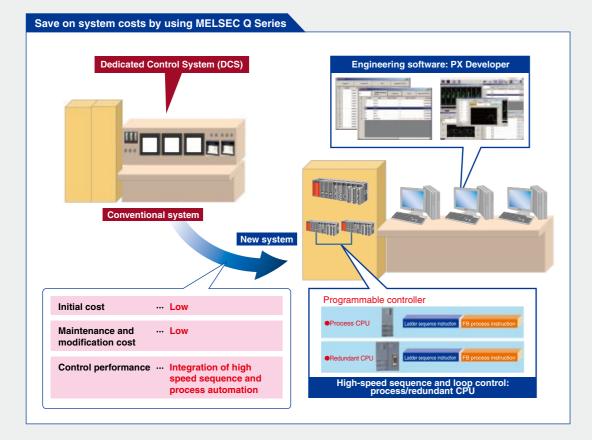




Needs & Solutions



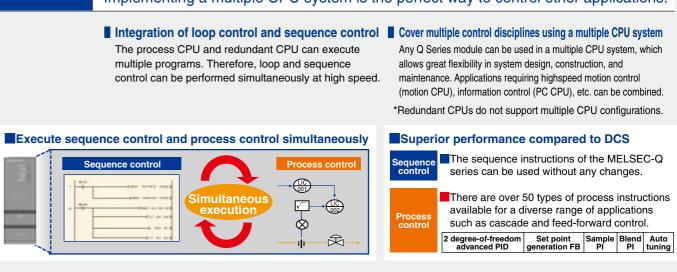
Needs	Design and create a process control system using programmable controllers.		
	Similar functionality to a DCS with high speed operations	Reduced system cost (low initial investment, maintenance, and modification)	
	Easy to create loop control programs	Same level of reliability compared to DCS	
Solutions	MELSEC process control provides loop analog processing, and easy to use eng redundant systems using two CPUs, two	ineering tools. Create fully	
	Loop control Process and redundant CPUs provide high-speed loop and sequence control with phenomenal reliability. Q series analog modules have many features including channel isolation, high accuracy, high resolution, and the ability to detect disconnections.	Maintenance Temperature control, analog, I/O, and other modules may be hot swapped; that is, they can be replaced while the system is powered on and running.	
	Simplified engineering PX Developer includes as standard, all of the necessary FBs (function blocks) for loop control. Loop control programs can be created quickly and easily by drag & drop operation. (No need for ladder programming) Additionally, tuning and monitoring each loop tag is made easy thanks to a standard interface.	Redundant system The redundant system (including the CPU module, power supply module, base unit, and network module) can maximize the system uptime. Special programming is not required, and can be done just like a regular system.	

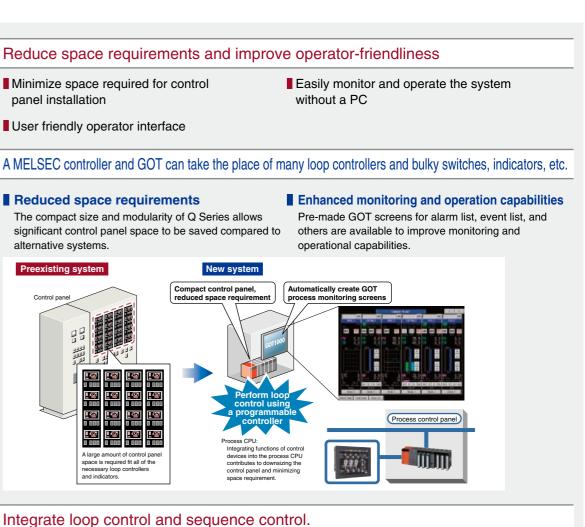


	User friendly operator interface		
Solutions	A MELSEC controller and GOT can take the place		
	Reduced space required The compact size and modular significant control panel space a alternative systems.	ity of Q Series allo	
	Preexisting system	New syste	
	Control panel	Compact control pany reduced space require Per Per a pro Process Cl Integrati devices i control p space re	
Needs	Integrate loop control a	nd sequence	
	There is a demand for an e sequence control. (Current	• •	
	Separate software required for each Complicated programming for data of		
	System scalability: expande	able to other ap	
Solutions	A single process CPU of Implementing a multiple		
	Integration of loop control a The process CPU and redunda multiple programs. Therefore, lo control can be performed simul	nt CPU can executor	
Execute seau	ence control and process cont	rol simultaneou	

Minimize space required for control

panel installation





ELSEC PROCESS CONTROL

REDUNDANT SYSTEM

modify, and maintain system that supports both loop and n uses separate controllers)

Program design, modification, and maintenance are time-consuming.

pplications such as motion control and information control.

both loop and sequence control. m is the perfect way to control other applications.

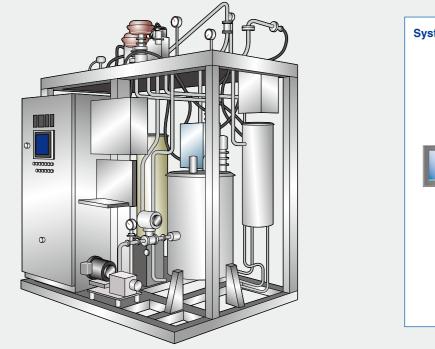


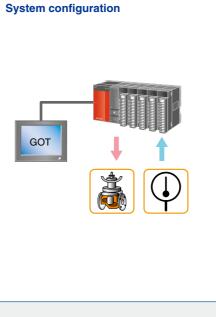






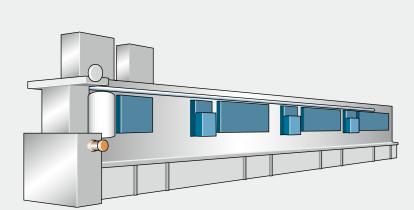
Applications previously requiring separate sequence and temperature control can now be controlled using a single process CPU. When a GOT is used in conjunction with the system, a space-saving, superior monitoring and control solution can be created.

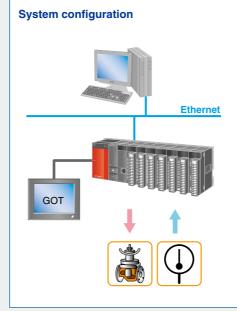




\bigcirc **Industrial furnace**

Implement process control using a wide variety of high-speed sequence and loop control instructions.

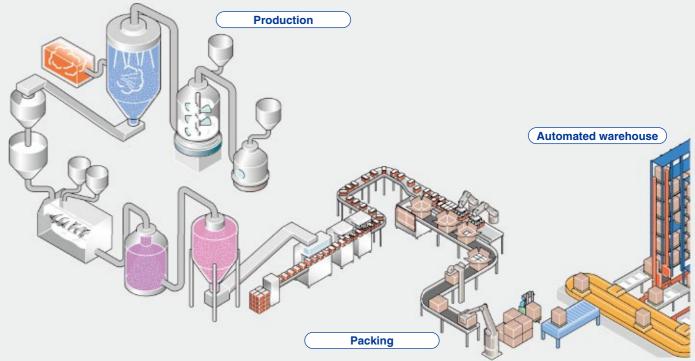


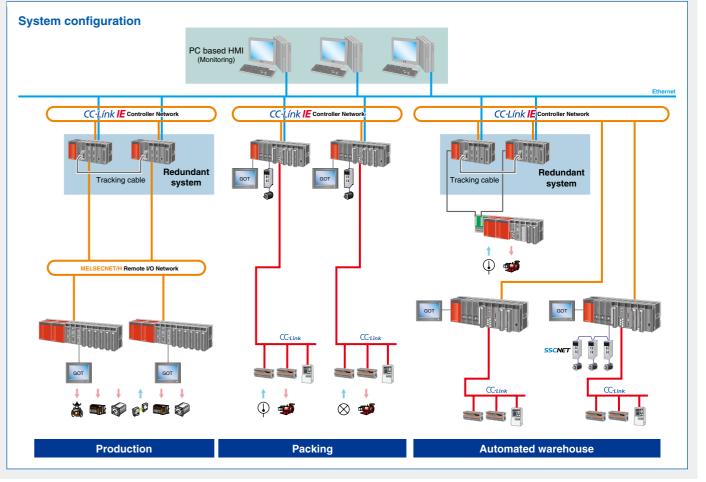


Fine chemical manufacturing

Mitsubishi Electric products help to provide integrated solutions for process automation and factory automation. Our process oriented products are well suited for manufacturing chemicals, such as medicines, paint, cosmetics, and detergent. Initial and running costs can be reduced compared to common alternatives.

REDUNDANT SYSTEM



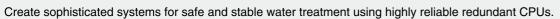


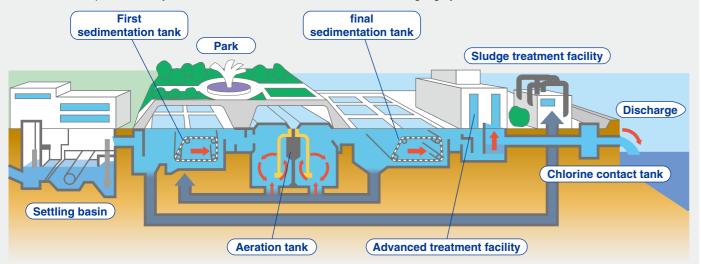


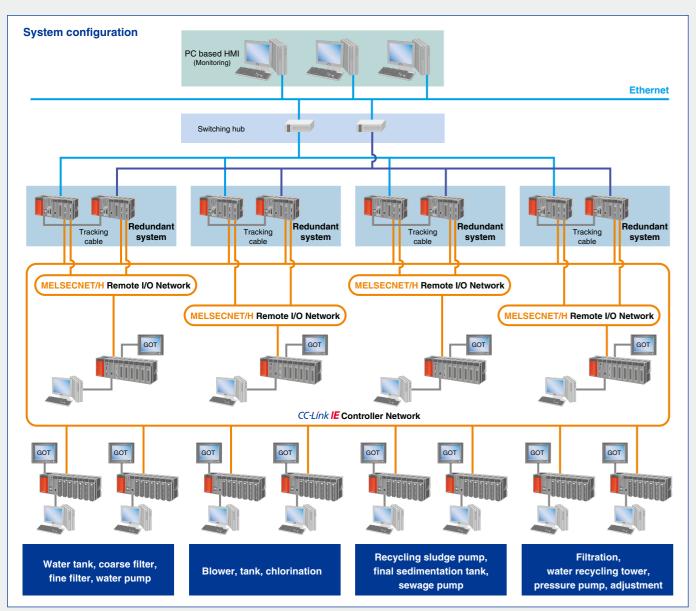
MELSEC PROCESS CONTROL/

REDUNDANT SYSTEM

Water treatment

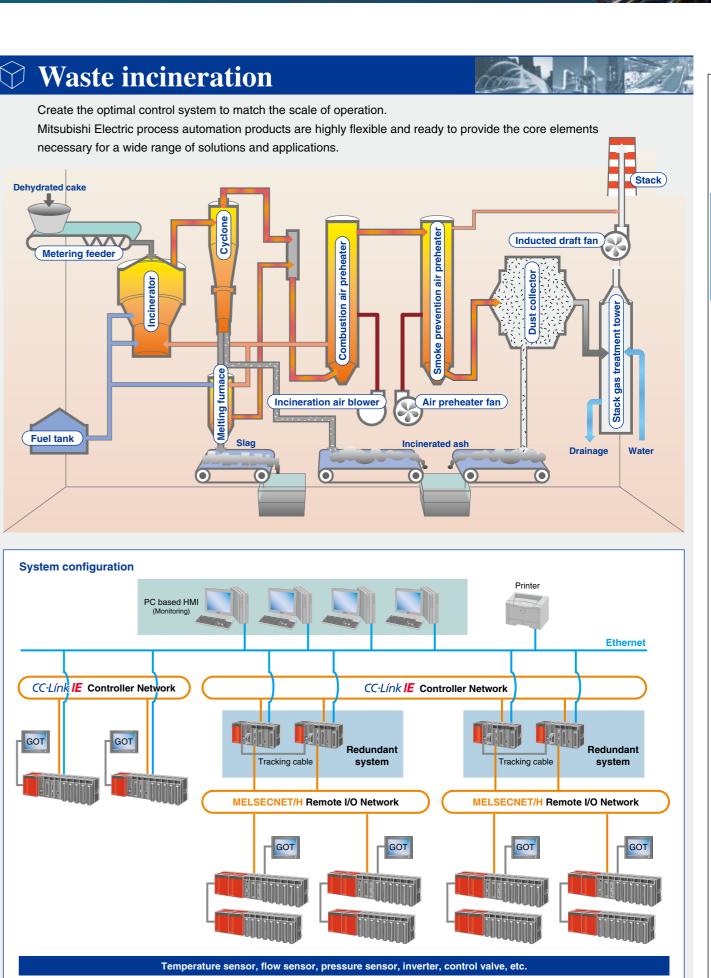


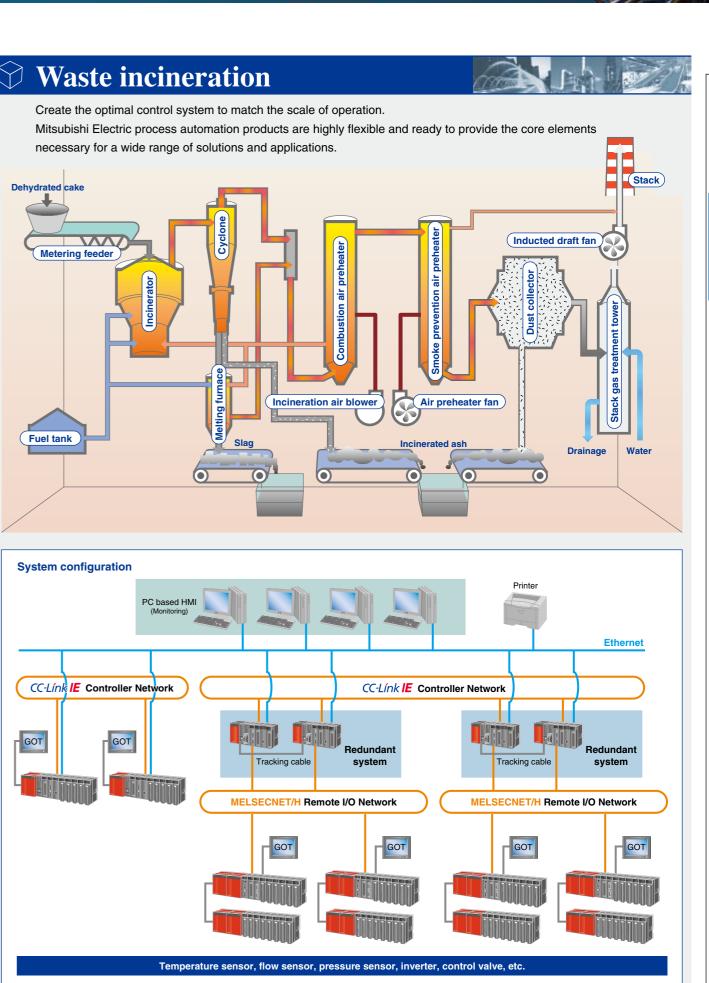




Waste incineration

necessary for a wide range of solutions and applications.





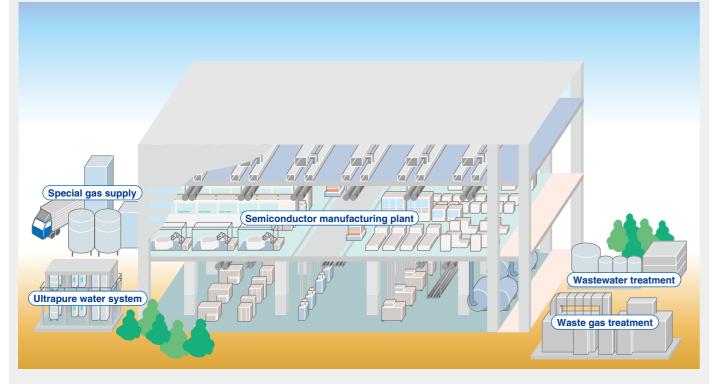


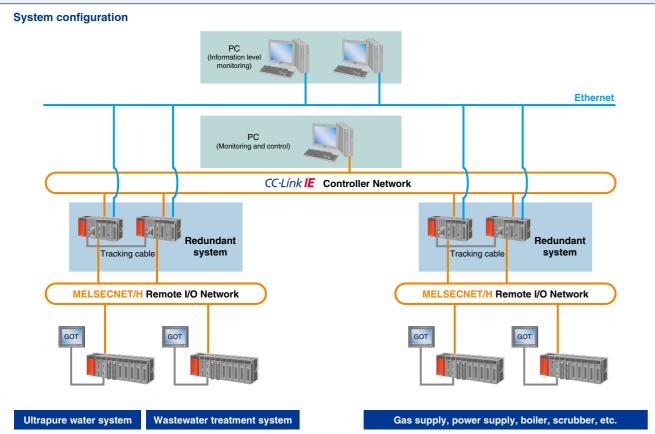


Semiconductor manufacturing



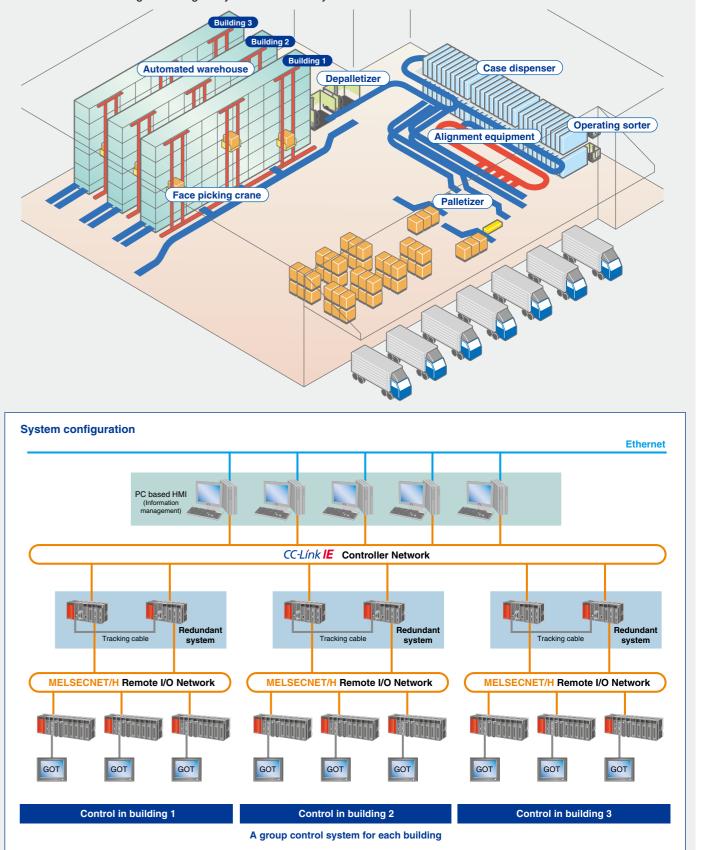
The dependability of purified water and gas supply systems for semiconductor applications can be increased by implementing a redundant system. Even if one programmable controller happens to fail, the other CPU is ready to immediately take over control and continue the operation without interruption.

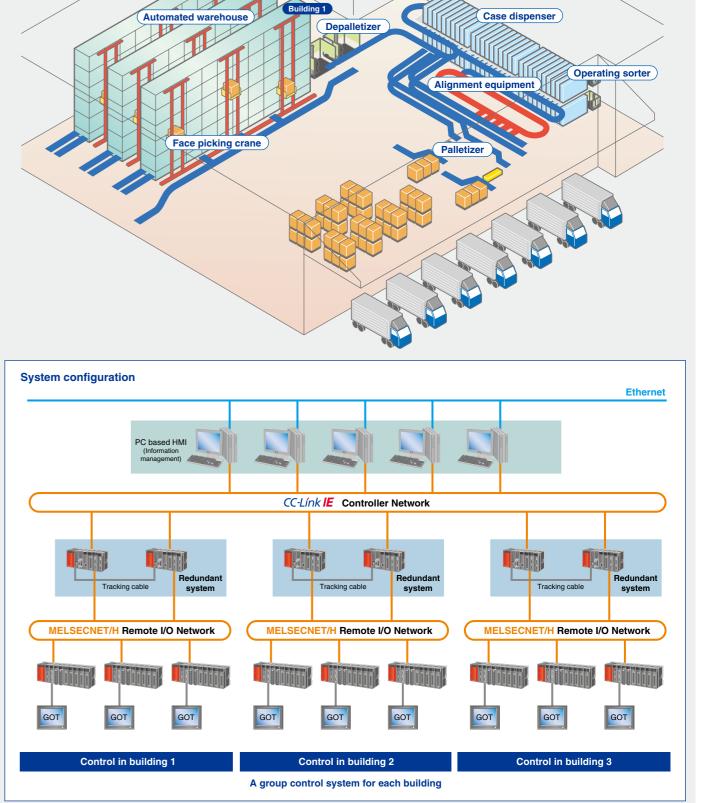




Material handling

reduction in loading/unloading delays due to control system issues.

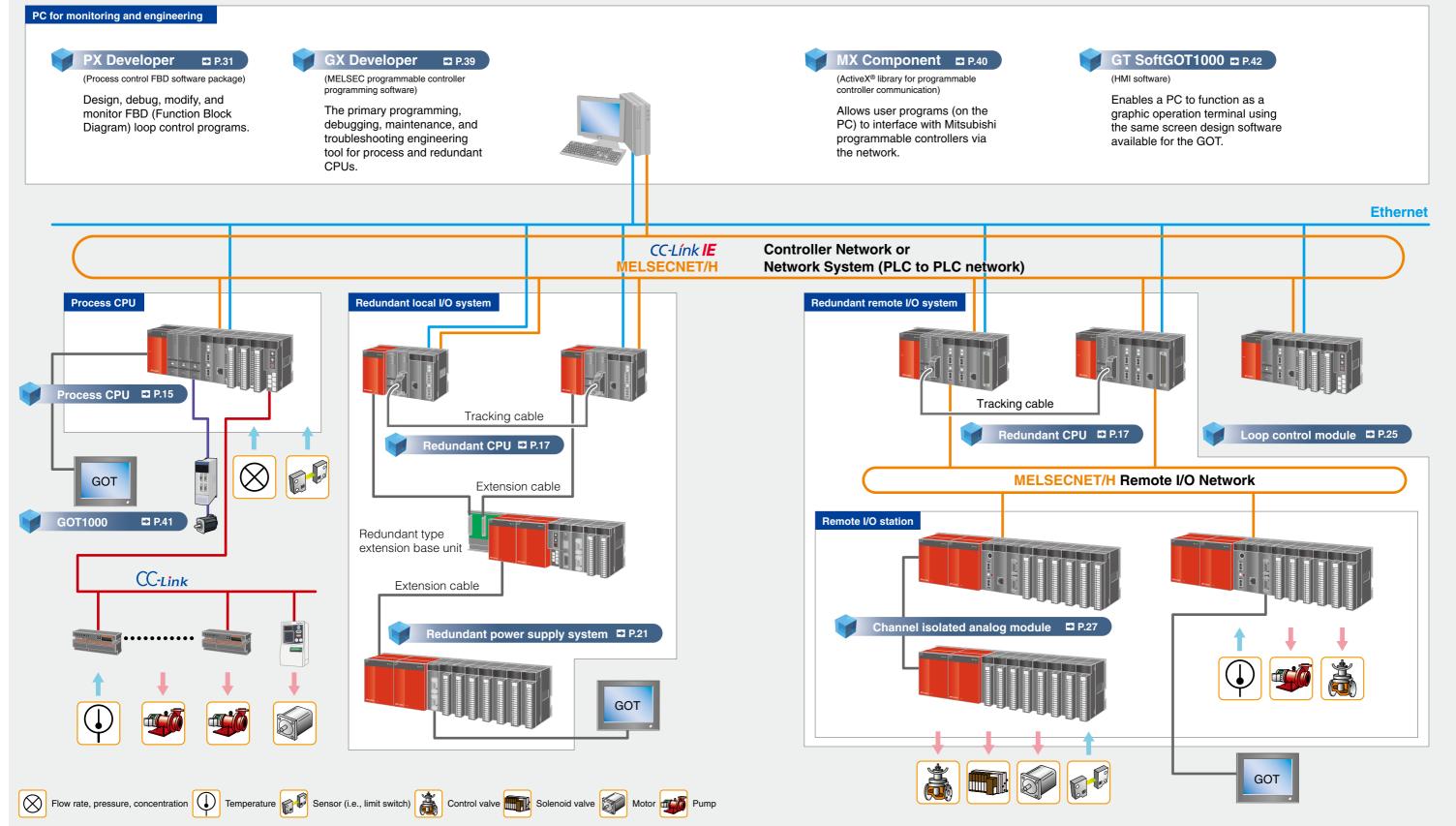






C PROCESS CONTROL REDUNDANT SYSTEM

Reduce costs by combining FA (Factory Automation) and PA (Process Automation) in the same platform.

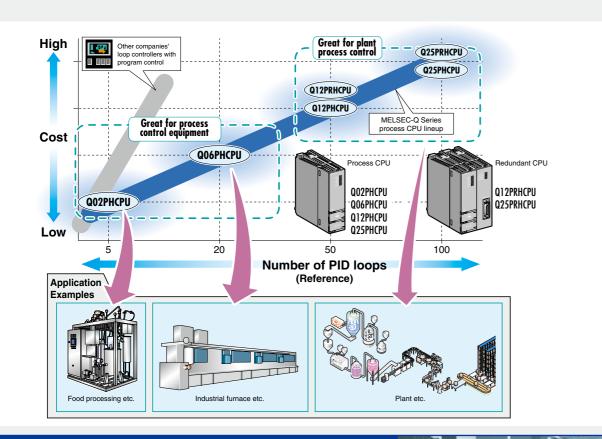




CPU (process & redundant)

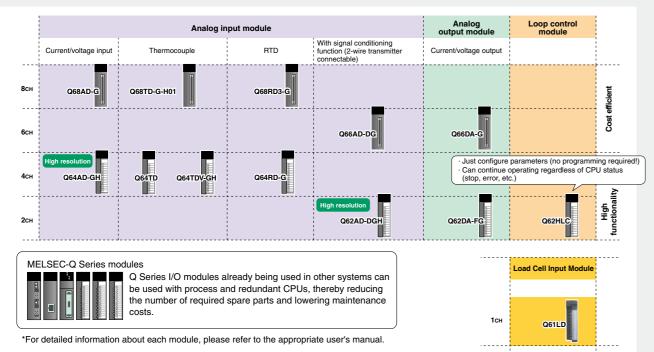


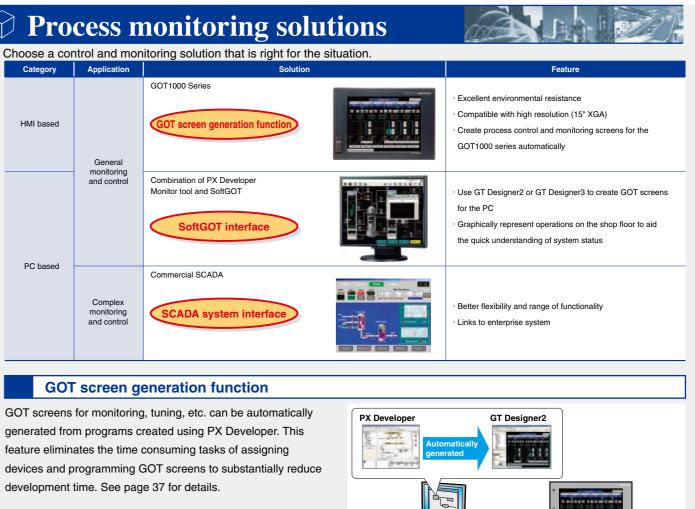
Choose the optimum CPU based on the number of PID loops required and the scale of the application.



\bigcirc Modules best suited for loop control

Choose from a wide selection of channel isolated analog modules for loop control.





GOT screens for monitoring, tuning, etc. can be automatically generated from programs created using PX Developer. This feature eliminates the time consuming tasks of assigning devices and programming GOT screens to substantially reduce development time. See page 37 for details.

SoftGOT interface

PX Developer monitoring functions can be 'called' or opened directly from GT SoftGOT1000 screens. Consequently, the development time for creating GOT screens can be reduced.

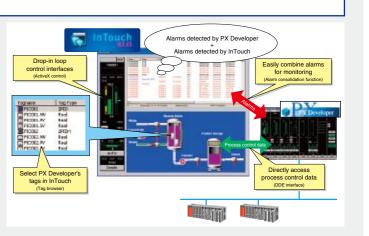
SCADA system interface

SCADA systems such as Wonderware InTouch by Invensys Systems, Inc. can be used to create advanced graphical displays of the system status. The PX Developer monitoring tool is designed to interface with SCADA software and its monitoring functions may be called and opened directly by the SCADA software.

The company and product names above are trademarked by their respective companies.

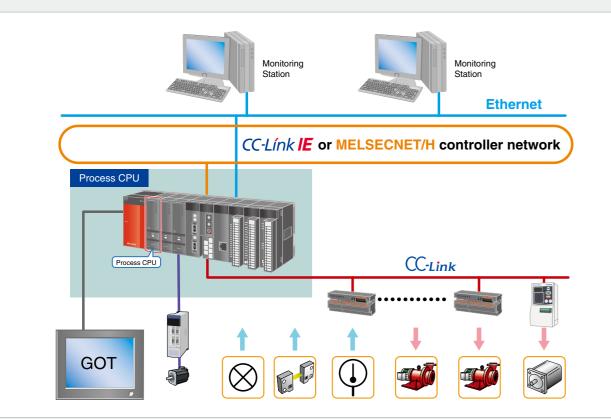
EC PROCESS CONTROL REDUNDANT SYSTEM







Process CPU



Features

Integrated loop control and sequence control

- · A single CPU can execute multiple programs simultaneously, thus loop control and sequence control programs can be processed at high-speed by the same CPU unit.
- Process CPUs may be used in multi-CPU systems to cover different areas of control such as high-speed motion control (motion CPU), or information control (PC CPU).

0 Extensive loop control

· The MELSEC Process CPU features 52 instructions dedicated for process control including a two-degree-of-freedom PID, sample PI, auto-tuning, and other instructions to support loop control.

3 High-speed loop control

With high-speed loop processing of approximately 400 μs (2-degree-of-freedom PID), control cycles as short as 10 ms can be achieved. Therefore, more loops can be executed simultaneously and applications requiring high-speed control cycles can be satisfied.

Improved reliability and maintenance features

- When an analog or I/O module fails, it can be replaced without stopping or turning off the CPU. (Note 1) (GX Developer is required.)
- Output behavior (hold previous state, etc.) in the event the CPU stops due to error can be set via parameter.

Simple engineering J

Create process control programs using PX Developer, an easy-to-use FBD language engineering tool.

Compatibility and expandability 0

The process CPU is compatible with all Q Series modules and is therefore well suited to take on a wide variety of applications.

Ample CPU lineup

From small machines to plant-wide process control, a CPU is available to match the application.

Improved total system throughput Ŏ

In combination with the high-speed, high-bandwidth CC-Link IE controller network, operations involving remote I/O stations and other networked controllers benefit drastically by improved response time and overall productivity.

Note 1) Online module change function (Function version restrictions)

Product name	Restrictions
Input module	
Output module	No restrictions
I/O composite module	
Analog input module	
Analog output module	
Temperature input module	Version C
Temperature control module	
Channel isolated pulse input module	

Note 2) To be compatible with the CC-Link IE network module, the first five digits of the CPU module's serial number must be 10042 or later.

Specifications

Item			Q02PHCPU	Q06PHCPU	Q12PHCPU	Q25PHCPU	
Control method					Sequence progra	m control method	
I/O control mode					Refr		
Sequence control language			Ladder, lis				
Program language	Pro	cess control langu	Jage	Process control FBD (Note 1)			
	LD inst		LD instruction		34	ns	
		uence ruction (Note 2)	MOV instruction	102 ns			
Processing speed	11151		Floating point addition	782 ns			
	Pro	cess instruction	2 degree of freedom PID		400	μs	
	(loo	p process time)	Basic PID		350	μs	
Dragram canacity	Nur	nber of steps		28 k steps	60 k steps	124 k steps	252 k steps
Program capacity	Nur	nber of programs		28	60	124	252 (Note 3)
Duilt in more and	Sta	ndard RAM		128 k	bytes	256 k	bytes
Built-in memory	Sta	ndard ROM		112 k bytes	240 k bytes	496 k bytes	1008 k bytes
	Pro	cess control instru	uctions		5	2	
Loop control	Cor	ntrol cycle		10 m	s or more/control loop	(setting available pe	r loop)
specifications	<u> </u>	n functions			PID control, cascade con	· •	
Number of I/O devic	e poir	nts (Note 4)				points	,
Number of I/O point						points	
Internal relay [M]						points	
Latch relay [L]						points	
Link relay [B]						points	
Timer [T]						points	
Retentive timer [ST]						pints	
Counter [C]	(Note	6)			· · ·	points	
Data register [D]				12288 points			
Link register [W]				8192 points			
Annunciator [F]				2048 points			
Edge relay [V]				2048 points			
Edge foldy [1]				Max 65536 points (can be used by block	Max. 131072 points	can be used by bloc
		Standard RAM			768 points (R0 to 32767)		
		SRAM card (1 M	IB)	-	can be used by block sw	-	
	[R]	SRAM card (2 M	,		can be used by block sw		
		Flash card (2 M			can be used by block sy	*	
		Flash card (4 M	,		can be used by block sy		
File register		Thaon ours (Thin	-/		(R0 to 65535),		(R0 to 131071),
		Standard RAM			ng not required	-	ig not required
		SRAM card (1 M	IB)		points (R0 to 517119)		
	[ZR]	SRAM card (2 M	,			•	•
		Flash card (2 MB)		1041408 points (R0 to 1041407), block switching not required 1041408 points (R0 to 1041407), block switching not required			
		Flash card (4 M	,	1042432 points (R0 to 1042431), block switching not required			
Link special relay [S	Bl		-1	2048 points (R0 to 1042431), block switching not required			
Link special register	-				2048		
Step relay [S]	[011]				8192		
Index register [Z]					16 p		
Pointer [P]					4096		
Interrupt pointer [I]					256 p		
Special relay [SM]					2048		
Special register [SD	1				2048		
Function input [FX]					16 p		
Function output [FY]			16 p				
Function register [FI	-				5 pc		
Number of mountab	-	U modules			Max		
Number of mountab					Max		
Number of extension base stages				Max			
					IVICA		
te 1) PX Developer is requi te 2) The processing time is							
te 3) Up to 124 programs c		•					
, .			an unit and autonaian base units direct	tly controlled by the CPLI mo	dule and the number of I/O po	ints controlled as remote I/O	via the remote I/O netwo
te 4) Indicates the total nun	IDELOLI	o points on the main be	ase unit and extension base units direct	ay controlled by the Or O mo	uule allu lile liullibei ol 1/0 po		via the remote i/O netwo

Software packages

PX Developer is used together with GX Developer.
The following software versions are required for
programming the process CPU.



uct name	w/ CC-Link IE	w/o CC-Link IE connection	
	connection	Q02/06PHCPU	Q12/25PHCPU
eveloper	Version 8.68W or later		Version 7.20W or later
eveloper	Version 1.18U or later		Version 1.00A or later



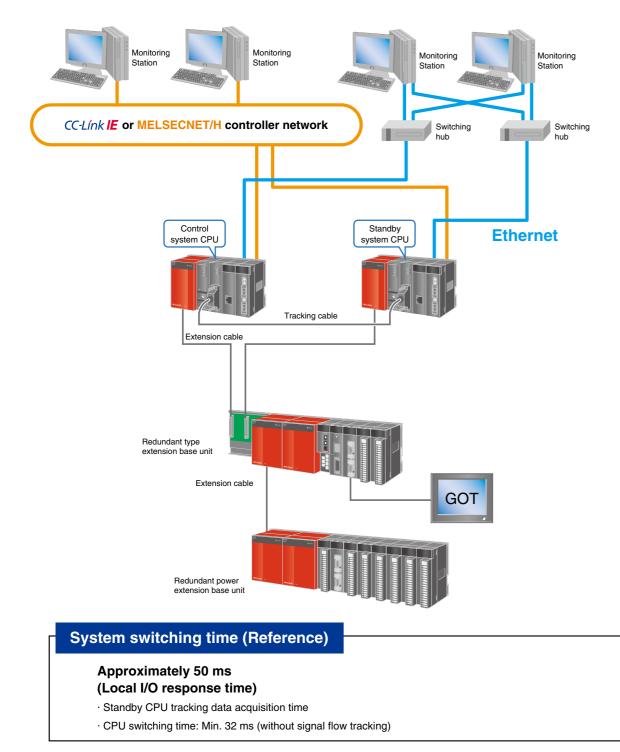
Redundant CPU \bigcirc



Redundant CPU, network, and power supply systems are provided to support various system configurations specific to application requirements.

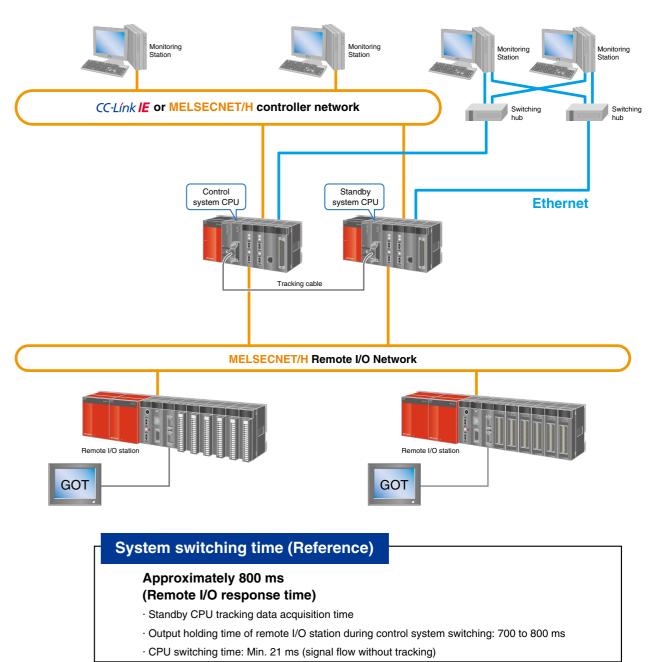
Redundant local I/O system

The CPU directly accesses I/O modules. Ideal for systems requiring high-speed response.



Redundant remote I/O system

Ideal for distributed systems with multiple remote I/O stations.



MELSEC PROCESS CONTROL REDUNDANT SYSTEM



Redundant CPU

Features

Basic system/network with redundancy

- · The basic system, including the CPU module, power supply module, base unit, and network module, can be configured with redundancy.
- · Networks can be configured with redundancy. When the network module fails or cable disconnection is detected, the standby system continues control and communications.

^ Improved reliability and maintainability

- · With the redundancy of the basic system, the standby system takes over the control to continue the system operation when the control system fails. (Hot standby)
- · By replacing the failed module or entire system, the redundant system can be recovered quickly.
- · When an I/O, analog I/O, temperature input, temperature control, or channel isolated pulse input module on the redundant extension base unit or remote I/O station fails, it can be replaced without stopping or turning off the CPU. (Note 1, 2) (Operation on GX Developer is required.)
- · When the CPU module is replaced while the system is operating, the parameters and programs can be copied to the new CPU by transfer instruction from GX Developer.

Loop control and sequence control in one CPU

· A single CPU can execute multiple programs simultaneously, thus loop control and sequence control programs can be processed at high-speed by the same CPŬ unit.

Simple engineering 4

- GX Developer offers simple engineering environment for redundant system settings with the original operability.
- PX Developer facilitates creating loop control programs using FBD language.
- Writing parameters and programs is simple using GX Developer regardless of the system status (control/ standby).

Employs existing Q Series modules

The Q Series modules, such as I/O, intelligent function, and network modules, can be used without any changes. (Note 3)

Improved total system throughput Ο

In combination with the high-speed, high-bandwidth CC-Link IE controller network, operations involving remote I/O stations and other networked controllers benefit drastically by improved response time and overall productivity.

Note 1) The following modules on the extension base unit or remote I/O station can be replaced while online.

Product name	Restrictions	Product name	Restrictions
Input module		Analog output module	
Output module	No restrictions	Temperature input module	Version C or later
I/O composite module		Temperature control module	version C or later
Analog input module	Version C or later	Channel isolated pulse input module	
, maiog inpat modalo		endinier loolated palee inpat medale	

Note 2) When the redundant type extension base unit is used, I/O modules on the main base unit cannot be replaced while online. Note 3) Use the following serial No. or version for the redundant system.

Product name	Model	Serial No. or Version	
Redundant CPU	Q12PRHCPU	First five digits of the serial number are 09012 or later (when the redundant type extension base unit is used);	
neuuliuant Cr O	Q25PRHCPU	First five digits of the serial number are 10042 or later (when the CC-Link IE Controller Network module is used)	
CC-Link IE Controller Network	QJ71GP21-SX	No restrictions	
module	QJ71GP21S-SX		
	QJ71LP21-25		
MELSECNET/H master module	QJ71LP21S-25		
MEEOEONE I/II Master module	QJ71LP21G		
	QJ71BR11		
	QJ72LP25-25		
MELSECNET/H remote I/O module	QJ72LP25G		
	QJ72BR15		
	QJ71E71-B2	Version D or later	
Ethernet interface module	QJ71E71-B5		
	QJ71E71-100		
	Q81BD-J71LP21-25		
	Q80BD-J71LP21-25		
MELSECNET/H interface board	Q80BD-J71LP21S-25		
	Q80BD-J71LP21G		
	Q80BD-J71BR11		
CC-Link interface module	QJ61BT11N	First five digits of the serial number are 06052 or later (when it is mounted on the main base unit)	
MES interface module	QJ71MES96	First five digits of the serial number are 09012 or later	
Web server module	QJ71WS96		

The following functions are not available for the module mounted on the extension base unit. · Intelligent function module dedicated instructions

Interrupt pointer

Item				Q12PRHCPU	Q25PRHCPU	
Control method				Sequence progra	am control method	
I/O control mode					fresh	
Sequence control language		Ladder, list, ST, SFC				
Program language Process control language		Process control FBD (Note 1)				
	Sequence LD instruction		LD instruction	34 ns		
			MOV instruction	10	2 ns	
	Insi	ruction (Note 2)	Floating point addition		2 ns	
Processing speed	Pro	cess instruction	2 degree of freedom PID		0 μs	
	(loc	pp process time) Basic PID 350 μs Tracking execution time 48 k word device memory: 10 ms				
	_					
	Red	dundant function	(extended scan time)		ce memory: 15 ms	
Des sus as a site :	Nur	mber of steps		124 k steps	252 k steps	
Program capacity	Nur	mber of programs		124 252 (Note 3)		
D		ndard RAM		256 k bytes		
Built-in memory	Sta	ndard ROM		496 k bytes	1008 k bytes	
Leen control	Pro	cess control instru	uctions		52	
Loop control	Cor	ntrol cycle		10 ms or more/control loop	o (setting available per loop)	
specifications		in functions		2 degree of freedom PID control, cascade co		
Number of I/O device					points	
Number of I/O point					points	
Internal relay [M]					points	
Latch relay [L]					points	
Link relay [B]				8192 points		
Timer [T]				2048 points		
Retentive timer [ST]	(Note	6)		0 points		
Counter [C]	(,		1024 points		
Data register [D]				12288 points		
Link register [W]					points	
Annunciator [F]					points	
Edge relay [V]					points	
		Standard RAM			vitching in units of 32768 points (R0 to 32767	
		SRAM card (1 M	1B)		vitching in units of 32768 points (R0 to 32767	
	[R]	SRAM card (2 M			witching in units of 32768 points (R0 to 32767	
	1.1	Flash card (2 M		Max. 1041408 points can be used by block switching in units of 32768 points (R0 to 32767		
		Flash card (4 MI			Max. 1042432 points can be used by block switching in units of 32768 points (R0 to 32767)	
File register		Standard RAM	5)	131072 points (R0 to 131071), block switching not required		
		SRAM card (1 M	1B)	517120 points (R0 to 517119), block switching not required		
	[78]	SRAM card (2 M		1041408 points (R0 to 1041407), block switching not required		
	[]	Flash card (2 M		1041408 points (R0 to 1041407), block switching not required		
		Flash card (4 MI		1042432 points (R0 to 1042431), block switching not required		
Link special relay [S	B1	Thaon oard (Thin	5)		points	
Link special register					points	
Step relay [S]	[011]				points	
Index register [Z]						
Pointer [P]				16 points 4096 points		
Interrupt pointer [I]				256 points		
Special relay [SM]						
Special register [SD]		2048 points 2048 points				
Function input [FX]						
Function output [FY]		16 points 16 points				
Function register [F					oints	
Number of device tr		a worde			400 points	
Number of device tr					/stem not available)	
					x. 63	
Number of mountab					x. 63 ax. 7	
Number of extensio Number of remote I				-	-	
	1100	nis		8 192 points (max.	2048 points/station)	

ote 1) PX Developer is required to write programs using FE Note 2) The processing time is the same even when using indexed devices. Note 3) Up to 124 programs can be executed.

controlled as remote I/O via the remote I/O network. Note 5) Indicates the number of I/O points on the main base unit and extension base units directly controlled by the CPU module

Note 6) Indicates the default number of points. These can be changed via parameters.

Software packages

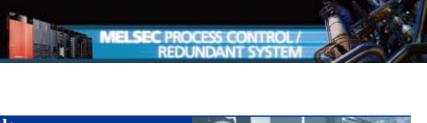
Specifications

PX Developer is used together with GX Developer. The following version or later is required for programming the redundant CPU.

PX De

Note 4) Indicates the total number of I/O points on the main base unit and extension base units directly controlled by the CPU module and the number of I/O points

		w/o CC-Link	E connection
luct name	w/ CC-Link IE connection	n Redundant type extension Redundant type extens	Redundant type extension
		base unit used	base unit not used
Developer	Version 8.68W or later	Version 8.45X or later	Version 8.18U or later
eveloper	Version 1.18U or later	Version 1.14Q or later	Version 1.06G or later





Redundant power supply system



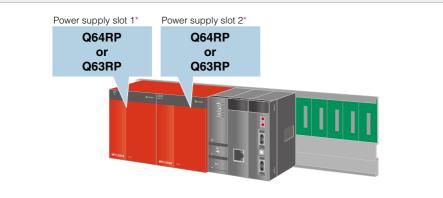
The redundant power supply system can be configured to back up the system in the event of a power failure.

Features

Redundant power supplies supporting all CPUs

1. Even if one power supply module fails, the other one supplies the power to the system.

- 2. A failed power supply module can be confirmed by a "power failure detection function" or "LED indicators", allowing for quick replacement. This ensures system backup.
- 3. The power supply module can be replaced while online.
- 4. Q64RP (AC input) and Q63RP (DC input) can be used together. Creating two power supply systems (AC and DC) further enhances system reliability.



* Either Q64RP or Q63RP can be mounted on the power supply slot 1 and 2. Also, in the event of a power failure, the power supply module can be replaced while online.

Specifications

Item		Q64RP	Q63RP	
Applicable base unit		Q38RB, Q68RB, Q65WRB		
Input power supply		100 to 120 V AC/200 to 240 V AC (+10%, -15%) (85 to 132 V AC/170 to 264 V AC)	24 V DC (+30%, -35%) (15.6 to 31.2 V DC)	
Input frequency		50/60 Hz ±5%	N/A	
Input voltage distor	tion rate	Within 5%	N/A	
Maximum input app	parent power	160 VA	N/A	
Maximum input pov	ver	N/A	65 W	
Inrush current		20 A, 8 ms or less	150 A, 1 ms or less	
Rated output currer	nt	8.5 A		
Overcurrent protect	tion	9.35 A or more		
Overvoltage protect	tion	5.5 to	6.5 V	
Efficiency		65% or more		
Allowable momenta	ary power failure period	20 ms or less	10 ms or less (at 24 V DC input)	
	Application	ERR contact		
	Rated switching voltage/current	24 V DC/0.5 A		
	Minimum switching load	5 V DC, 1 mA		
Contact output	Response time	OFF to ON: 10 ms or less, ON to OFF: 12 ms or less		
Contact output	Life	Mechanical: 20,000,000 times or more Electrical: 100,000 times or more at rated switching voltage and current		
	0	Electrical: 100,000 times of more at		
	Surge suppressor		-	
	Fuse	No		

Fiber optic loop network module with external power supply function

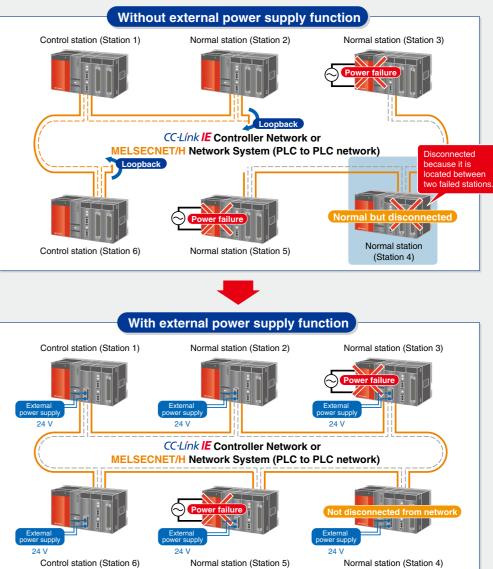
The external power supply function enables the system to continue data link when the power supply module fails.

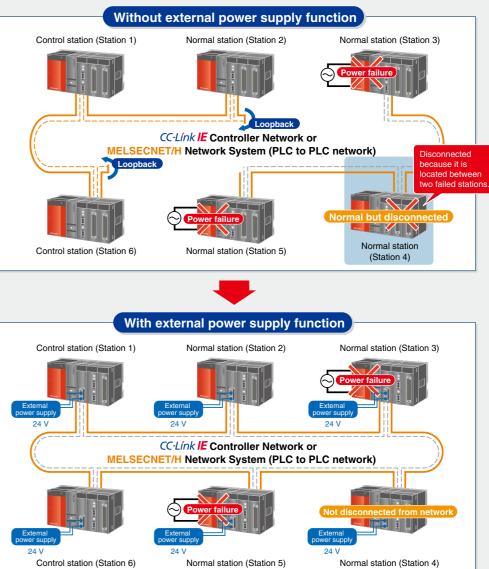
Features

External power supply prevents the system from being affected by a power failure

Even if a power failure occurs at more than two stations in a loop system, a station in between can continue data link. This function also prevents loopback in the system. The link scan time can be stabilized, ensuring steady system operation.

When a power failure occurred in two stations:





Specifications

Item		CC-Línk IE Controller Network module QJ71GP21S-SX	MELSECNET/H network module QJ71LP21S-25	
	Voltage	20.4 to 31.2 V DC		
	Current	0.28A	0.20A	
	Terminal screw size	M3		
External	Applicable solderless terminal	R1.25-3		
power supply	Applicable wire size	0.3 to 1.25 mm ²		
	Tightening torque	0.42 to 0.58 N·m		
	Allowable momentary power failure time	1 ms (Level PS1)		
	Noise immunity	By noise simulator of 500Vp-p noise voltage, 1µs pulse width, and 25 to 60Hz noise frequency		



Multiplexed remote I/O network system



Redundant system with superior cost effectiveness by using highly reliable, high-speed network.

🔰 Features

Facilitating the multiplexed remote master station and multiplexed remote sub-master station on one remote I/O network system enables the multiplexed remote sub-master station to control the remote I/O network system instead when the programmable controller CPU in the multiplexed remote master station becomes faulty.

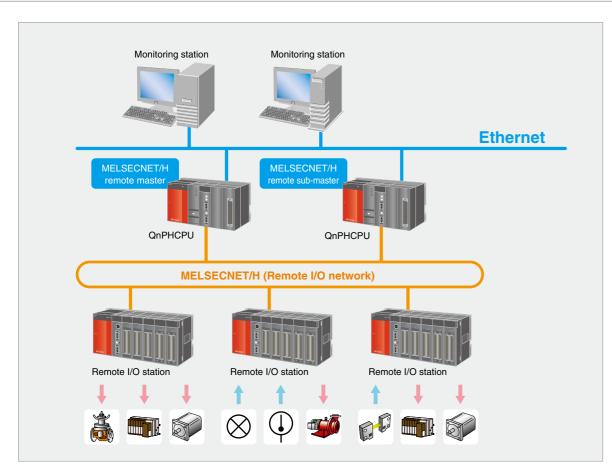
The multiplexed remote master station can return to normal and to system operation as a multiplexed remote sub-master station, even during the remote I/O network system control by the multiplexed remote sub-master station, thus preparing itself for a multiplexed remote sub-master station.

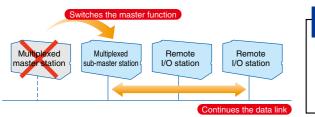
System configuration

■Usable CPU is Process CPU (QnPHCPU).

The redundant system configured with QnPHCPU + MELSECNET/H (remote I/O network). (Note 1)

The multiplexed remote function enables the multiplexed remote sub-master station to continue the I/O working when the multiplexed remote master station becomes faulty due to a malfunction in power supply, etc. (Note 1) When tracking is needed, the communication module (Ethernet, etc.) to communicate the tracking data and the creation of the user program for tracking is required.







700 to 800ms (when connecting 4 remote I/O stations) Changed by sequence scan time, link scan time.

CC-Link Master station duplex function

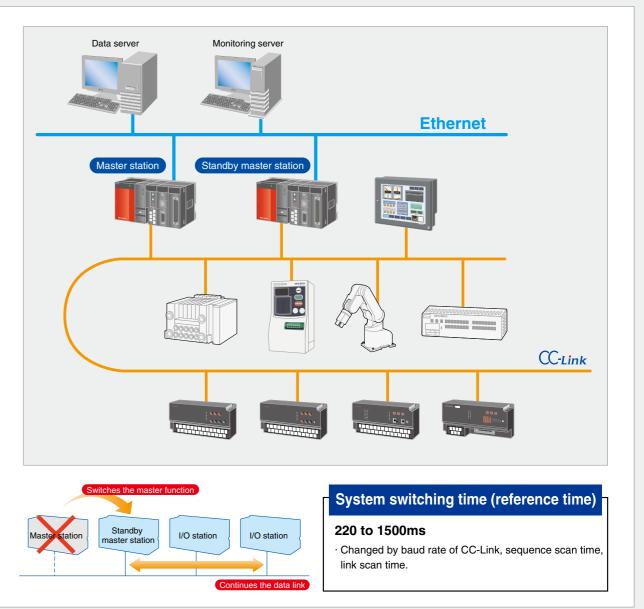
Cost-saving redundant system by using open field network.

Features

to a malfunction in the programmable controller CPU or power supply. the standby master station, thus preparing itself for the standby master station system down.

System configuration

- master station. (Note 2)
- The CC-Link Master station duplex function enables the standby master station to continue the data link working when the master station becomes faulty. The master station can return to normal and to system operation as the standby master station, even during data-link control by the standby master station. Note 2) When tracking is needed, the communication module (Ethernet, etc.) to communicate the tracking data and the creation of the user program for tracking is required.



REDUNDANT SYSTEM

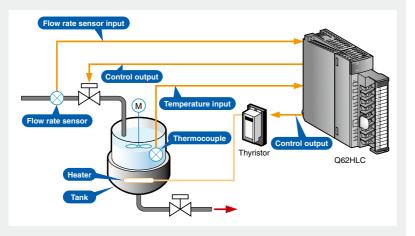
- The CC-Link Master station duplex function enables the data link to continue working by switching the control to the standby master station (meaning a backup station for the master station) automatically if system down occurs in the master station due
- The master station can return to normal and to system operation as the standby master station, even during data-link control by

The redundant system can be configured in CC-Link network regardless of the CPU types of the master station, standby



Loop control module

- Ideal for fast response control such as
- · Rapid temperature increase control in flip chip bond IC manufacturing
- · Drying oven cooling temperature control on freeze drying machines
- •Staggering 25 ms sampling and control update time, industry's fastest.
- Supports sensor types, such as thermocouple, microvoltage, and current input ranges.
- Continuous PID control by 4 to 20 mA current output results in highly stable and accurate control.



Control program profiles can be specified where set

values (SV) and PID constants (Proportional band,

Integral time, Derivative time) are automatically changed

Cascade control can be performed with channel 1 as the

SV Program control using 16-segment

program profile

Program control function

Cascade control function

master and channel 2 as the slave. Resin nozzle temperature control

at specified times.

Q62HLC

J

Features

High-speed PID control

The Q62HLC loop control module performs a continuous PID control and supports thermocouple inputs, microvoltage inputs, voltage inputs, current inputs, and current outputs. These features make the Q62HLC ideal for fast response control.

Connectable to thermocouples complying with major international standards

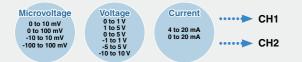
Thermocouples complying with the JIS, IEC, NBS, and ASTM standards are supported.

Items complying with any of these can be used



Supports a variety of input ranges J

The use of an input sensor (microvoltage, voltage, and current inputs) enables analog value measurements in the ranges shown below.



Specifications

Item		ltem	Specifications					
Nu	mber of analog I/O po	ints	2 channels/module					
		Analog input spec	ifications	Analog output specifications				
Nu	mber of input points		2 points (2 channels)	Number of	f output points	2 points (2 channels)		
An	alog input		See (2)	Digital inpu	ut	16-bit signed binary		
Dig	ital output		16-bit signed binary	Analog out	tput	Current		
Us	able thermocouples		K, J, T, S, R, N, E, B, PL II, W5Re/W26Re		-	-		
Inp	ut characteristics		See (1)	Output cha	aracteristics	Digital input value: 0 to 1000 (0 to 4000 when using simplified analog output) Output range: 4 to 20 mA		
Ma	ximum resolution		See (1)	Maximum resolution		4 μΑ		
de 2)	Indication accuracy	Ambient temperature: $23^{\circ}C \pm 2^{\circ}C$	See (2) (a)	Output	Ambient temperature: $23^{\circ}C \pm 2^{\circ}C$	Full scale x (±0.2%)		
Š	indication accuracy	Ambient temperature: 0°C to 55°C	See (2) (b)	accuracy	Ambient temperature: 0°C to 55°C	Full scale x (±0.4%)		
oura	Cold junction temperature	Ambient temperature: 23°C \pm 2°C	±0.5°C					
Acc	compensation accuracy	Ambient temperature: 0°C to 55°C	±1.0°C		-	-		
Co	nversion speed		25 ms/2channels (constant regardless of the number of channels used)	Conversion speed		25 ms/2channels (constant regardless of the number of channels used)		
Sa	mpling period		25 ms/2channels (constant regardless of the number of channels used)	Control update time		25 ms/2channels (constant regardless of the number of channels used)		
Ab	solute maximum input		Microvoltage: \pm 12 V, voltage: \pm 15 V, current: \pm 30 mA	Allowable load resistance		600 Ω or less		
Inp	ut impedance		Thermocouple, microvoltage, voltage: 1 M\Omega, current: 250 Ω	Output impedance		5 MΩ		
Note	lote 2) Accuracy is calculated as follows: [Accuracy] = [Indication accuracy] + [Cold junction temperature compensation accuracy]							

Note 2) Accuracy is calculated as follows: [Accuracy] = [Indication accuracy] + [Cold junction temperature compensation accuracy]

(1) Usable input sensor types, measurement ranges, and data resolution

	Input	Input range	Digital value	Resolution
	К	-200 to 1372°C	-2000 to 13720	
	J	-200 to 1200°C	-2000 to 12000	
	Т	-200 to 400°C	-2000 to 4000	
	S	-50 to 1768°C	-500 to 17680	
Thermocouple	R	-50 to 1768°C	-500 to 17680	0.1°C
Thermocoupie	N	0 to 1300°C	0 to 13000	1
	E	-200 to 1000°C	-2000 to 10000	1
	В	0 to 1800°C	0 to 18000	
	PL II	0 to 1390°C	0 to 13900	1
	W5Re/W26Re	0 to 2300°C	0 to 23000	
		0 to 10 mV	0.4- 00000	0.5 μV
Microvoltage		0 to 100 mV	0 to 20000	5 μV
wiiciovoltage		-10 to 10 mV	-10000 to 10000	1 μV
		-100 to 100 mV	-10000 to 10000	10 µV
		0 to 1 V		0.05 mV
		1 to 5 V	0 to 20000	0.2 mV
		0 to 5 V	0 10 20000	0.25 mV
Voltage		0 to 10 V		0.5 mV
		-1 to 1 V		0.1 mV
		-5 to 5 V	-10000 to 10000	0.5 mV
		-10 to 10 V		1 mV
Current		4 to 20 mA	0 to 20000	0.8 µA
Current		0 to 20 mA	0 10 20000	1 μA

(2) Indication accuracy

(a) At ambient temperature of $23 \pm 2^{\circ}C$

	Item	Error		
		Less than -100°C	± 1.0°C	
	K, J, T, E, PL II	-100 to less than 500°C	± 0.5°C	
		500°C or more	± [Indication value x (0.1%) + 1 digit]	
Thermosourle	S, R, N,	-50 to less than 1000°C	± 1.0°C	
Thermocouple	W5Re/W26Re	1000°C or more	± [Indication value x (0.1%) + 1 digit]	
		Less than 400°C	± 70.0°C	
	В	400 to less than 1000°C	± 1.0°C	
		1000°C or more	± [Indication value x (0.1%) + 1 digit]	
Microvoltage		·		
Voltage		Full scale x (±0.1%)		
Current				

(b) At ambient temperature of 0 to 55°C						
	Item	Error				
		Less than -100°C	± 2.0°C			
	K, J, T, E, PL II	-100 to less than 500°C	± 1.0°C			
		500°C or more	± [Indication value x (0.2%) + 1 digit]			
Thermocouple	S, R, N,	-50 to less than 1000°C	± 2.0°C			
mermocoupie	W5Re/W26Re	1000°C or more	± [Indication value x (0.2%) + 1 digit]			
		Less than 400°C	± 140.0°C			
	В	400 to less than 1000°C	± 2.0°C			
		1000°C or more	± [Indication value x (0.2%) + 1 digit]			
Microvoltage Voltage						
			Full scale x (±0.2%)			
Current						

ELSEC PROCESS CONTROL REDUNDANT SYSTEM



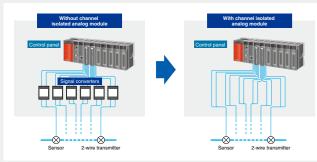
Channel isolated analog modules

A wide selection of channel isolated analog modules are provided to meet requirements for process control and highaccuracy control.

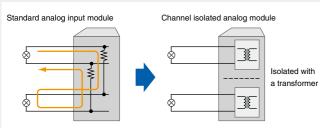
Features



External signal converters are not required.

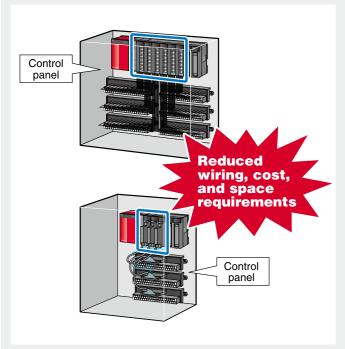


Electric disturbances such as current and noise can be isolated.



Cost and space requirement reduction (multi-channel type)

With multi-channel modules, more cost effective and small footprint systems can be configured.

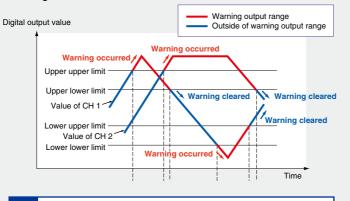


Disconnection detection function

When the analog output range is 4 to 20 mA or the user range setting of current, disconnection is detected by monitoring output values.

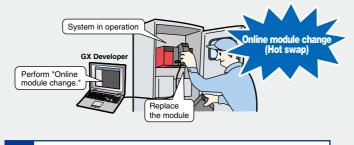
Warning and error detection functions

Analog modules monitor analog input signals and notify warnings and errors.



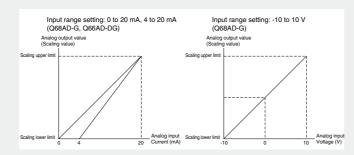
Online module change (hot swap)

Even if the analog module fails during operation, it can be replaced without stopping the system.



Scaling function (Q68AD-G, Q66AD-DG, Q66DA-G, Q68TD-G-H02)

A value input from an external device can be converted to an arbitrary value. This function eliminates the need for a ladder program that converts A/D conversion data to an actual physical value.



Specifications

Channel isolated high resolution analog input module

	Item	Q64AD-GH
No. of analog	input points	4 points (4 channels)
Analog input	Voltage	0 to 5 V DC, 1 to 5 V DC, 0 to 10 V DC, -10 to 10 V DC, user range
Analog input	Current	0 to 20 mA DC, 4 to 20 mA DC, user range
Absolute max	. input	±15 V, ±30 mA
Digital	32-bit	0 to 64000 (0 to 5 V DC, 1 to 5 V DC, 0 to 10 V DC; 0 to 20 mADC, 4 to 20 mADC -64000 to 64000 (-10 to 10 V DC)
output	16-bit	0 to 32000 (0 to 5 V DC, 1 to 5 V DC, 0 to 10 V DC; 0 to 20 mA DC, 4 to 20 mA DC -32000 to 32000 (-10 to 10 V DC)
Accuracy (accuracy to max. digital output	Reference accuracy	±0.05% Digital output value (32-bit): ±32 digits Digital output value (16-bit): ±16 digits
value)	Temperature coefficient	±71.4 ppm/°C (0.00714%/°C)
Conversion speed		10 ms/4 channels
Isolation method		Between I/O terminal and programmable controller power supply: Photocoupler Between analog input channels: Transformer
No. of occupie	ed I/O points	16 points (I/O assignment: Intelligent 16 points)
External conn	nections	18-point terminal block

Channel isolated analog input module

	Item	Q68AD-G		
No. of analog	input points	8 points (8 channels)		
Analog	Voltage	0 to 5 V DC, 1 to 5 V DC, 0 to 10 V DC, -10 to 10 V DC, user range		
input	Current	0 to 20 mA DC, 4 to 20 mA DC, user range		
Absolute max	c. input	±15 V, ±30 mA		
Digital	High resolution mode	0 to 16000 (0 to 10 V DC), 0 to 12000 (0 to 5 V DC, 1 to 5 V DC; 0 to 20 mA DC, 4 to 20 mA DC), -16000 to 16000 (-10 to 10 V DC), -12000 to 12000 (user range)		
output	Normal resolution mode	0 to 4000 (0 to 10 V DC, 0 to 5 V DC, 1 to 5 V DC; 0 to 20 mA DC, 4 to 20 mA DC) -4000 to 4000 (-10 to 10 V DC, user range)		
Accuracy (accuracy to max. digital output value)	Reference accuracy	±0.1% High resolution mode (0 to 10 V, -10 to 10 V): ±16 digits High resolution mode (other than the above ranges): ±12 digits Normal resolution mode: ±4 digits		
value)	Temperature coefficient	±71.4 ppm/°C (0.00714%/°C)		
Sampling cycle		10 ms/channel		
Isolation meth	hod	Transformer		
No. of occupi	ed I/O points	16 points (I/O assignment: Intelligent 16 points)		
External conr	nections	40-pin connector		

Channel isolated high resolution analog input module (with signal conditioning function)

ELSEC PROCESS CONTROL

REDUNDANT SYSTEM

			ltem	Q62AD-DGH	
ы	cations	No. of analog input points		2 points (2 channels)	
2-wire transmitter connection	Input specifications	Analog input		4 to 20 mA DC (input resistance: 250 Ω), user range	
smitte	pply	Supp	ly voltage	26±2 V DC	
e tran:	Power supply specifications	Max.	supply current	24 mA DC	
2-wire	o g	Short-circuit protection		Yes (limit current: 25 to 35 mA)	
	Ch	eck te	rminals	Yes	
Dig	Digital 32-bit			0 to 64000	
out	iput		16-bit	0 to 32000	
Accuracy (accuracy to max. dioital output				±0.05% Digital output value (32-bit): ±32 digits Digital output value (16-bit): ±16 digits	
valu				±71.4 ppm/°C (0.00714%/°C)	
Co	nver	sion s	peed	10 ms/2 channels	
Isolation method			nod	Between I/O terminal and programmable controller power supply: Photocoupler Between analog input channels: Transformer Between external power supply and analog input channel: Transformer	
No	. of c	ccupi	ed I/O points	16 points (I/O assignment: Intelligent 16 points)	
Ext	terna	l conn	ections	18-point terminal block	

Channel isolated analog input module (with signal conditioning function)

	Item			Q66AD-DG
ц	ations	No. of analog input points (no. of 2-wire transmitters)		6 points (6 channels)
2-wire transmitter connection	Input specifications	Analog input		4 to 20 mA DC (input resistance: 250 Ω), user range (0 to 20 mA DC without 2-wire transmitter)
smitte	yly ons	Supp	oly voltage	26±2 V DC
e tran	Power supply specifications	Max.	supply current	24 mA DC
2-wir	Pow	Short-circuit protection		Yes (limit current: 25 to 35 mA)
	Ch	eck te	rminals	Yes
Dig	ital		High resolution mode	0 to 12000
out	put		Normal resolution mode	0 to 4000
(acc		o max.	Reference accuracy	$\pm 0.1\%$ High resolution mode: ± 12 digits Normal resolution mode: ± 4 digits
valu	al outp e)	Temperature coefficient		±71.4 ppm/°C (0.00714%/°C)
Sa	mplir	ng cyc	le	10 ms/channel
lso	latio	n metł	nod	Transformer
No	. of c	ccupi	ed I/O points	16 points (I/O assignment: Intelligent 16 points)
Ext	erna	l conr	nections	40-pin connector

Specifications

Channel isolated analog output module

Item		Q62DA-FG	
No. of analog out	put points	2 points (2 channels)	
Digital input		16-bit signed binary (-12288 to 12287, -16384 to 16383)	
Analog output	Voltage	-12 to 12 V DC (external load resistance: 1 k to 1 M Ω)	
Analog output	Current	0 to 20 mA DC (external load resistance: 0 to 600 $\Omega)$	
Analog output	Voltage	1 to 5 V DC, 0 to 5 V DC, -10 to 10 V DC, user range setting 2, user range setting 3	
range	Current	0 to 20 mA DC, 4 to 20 mA DC, user range setting 1	
Accuracy	Reference accuracy	$\pm 0.1\%$ (voltage: ± 10 mV, current: $\pm 20~\mu\text{A})$	
(accuracy to max. analog output value)	Temperature coefficient	±80 ppm/°C (0.008%/°C)	
Conversion spee	d	10 ms/2 channels	
Absolute max.	Voltage	±13 V	
output	Current	23 mA	
	Resolution	12 bits	
Output monitor	Reference accuracy	±0.2%	
	Temperature coefficient	±160 ppm/°C (0.016%/°C)	
Short-circuit prote	ection	Yes	
Isolation method		Between I/O terminal and programmable controller power supply: Photocoupler Between analog output channels: Transformer Between external power supply and analog output channel: Transformer	
No. of occupied I	/O points	16 points (I/O assignment: Intelligent 16 points)	
External connections		18-point terminal block	

Item		Q66DA-G
No. of analog output points		6 points (6 channels)
		16-bit signed binary (high resolution mode: -12288 to 12287, -16384 to 16383; normal resolution mode: -4096 to 4095)
Analog output	Voltage	-12 to 12 V DC (external load resistance: 1 k to 1 MΩ)
Analog output	Current	0 to 20 mA DC (external load resistance: 0 to 600 Ω)
Analog output range	Voltage	1 to 5 V DC, 0 to 5 V DC, -10 to 10 V DC, user range setting 2, user range setting 3
range	Current	0 to 20 mA DC, 4 to 20 mA DC, user range setting 1
Accuracy	Reference accuracy	$\pm 0.1\%$ (voltage: ± 10 mV, current: $\pm 20~\mu\text{A})$
(accuracy to max. analog output value)	Temperature coefficient	±80 ppm/°C (0.008%/°C)
Conversion spee	d	6 ms/channel
Absolute max.	Voltage	±13 V
output	Current	23 mA
Short-circuit prot	ection	Yes
Isolation method		Between output terminal and programmable controller power supply: Transformer Between analog output channels: Transformer Between external power supply and analog output channel: Transformer
No. of occupied	I/O points	16 points (I/O assignment: Intelligent 16 points)
External connect	tions	40-pin connector

Channel isolated thermocouple/micro voltage input module and thermocouple input module

	Item	Q64TDV-GH	Q64TD	Q68TD-G-H02	
No. of cha	nnels	4 cha	nnels	8 channels	
Output	Temperature conversion value	16-bit signed bir	nary (-2700 to 18200: Each increment represents	0.1°C change)	
·	Scaling value		16-bit signed binary		
Thermoco	uple standards	JIS C160	02-1995	JIS C1602-1995, IEC 60584-1 (1995), IEC 60584-2 (1982)	
	ermocouples and re measurement range	B: 0 to 1820°C, R: -50 to 1760°C, S: -50 to 1760°C, K: -270 to 1370°C, E: -270 to 1000°C, J: -210 to 1200°C, T: -270 to 400°C, N: -270 to 1300°C			
Micro volta	age input range	-100 to 100 mV (input resistance: 2 MΩ or more)	N	/A	
	ion temperature tion accuracy	±1.0°C			
Conversio	n speed	Sampling cycle x 3	40 ms/channel	640 ms/8 channels	
Sampling	cycle	20 ms/channel	N/A		
No. of ana	log input points	(4 channels + Pt100 conr	(4 channels + Pt100 connection channel)/module (8 c		
Isolation method		Between thermocouple input/micro voltage input and ground: Transformer Between thermocouple input/micro voltage input channels: Transformer Between cold junction temperature compensation input (Pt100) and ground: Non-isolated			
Disconnection detection		Yes (each channe	Yes		
No. of writes to E ² PROM		Max. 100,0	N/A		
No. of writes to flash memory		N/.	Max. 50,000 times		
No. of occ	upied I/O points		16 points (I/O assignment: Intelligent 16 points)	·	
External connections		18-point terr	40-pin connector		

Channel isolated pulse input module

	Item	QD60P8-G
Counting	speed switch settings	30 kpps/10 kpps/1 kpps/100 pps/50 pps/10 pps/1 pps/0.1 pps
No. of ch	annels	8 channels
Countin	put signal	1 phase
Countin	Signal level	5 V DC/12 to 24 V DC
	Counting speed (max.)	30 kpps/10 kpps/1 kpps/100 pps/50 pps/10 pps/1 pps/0.1 pps
Counter	Counting range	Sampling pulse number: 16-bit binary (0 to 32767) Accumulating count value: 32-bit binary (0 to 99999999) Input pulse value: 32-bit binary (0 to 2147483647)
	Count type	Linear counter method, ring counter method
No. of oc	cupied I/O points	32 points (I/O assignment: Intelligent 32 points)
External	connections	18-point terminal block

Channel isolated RTD input module

	Item		Q64RD-G	Q68RD3-G			
No. of ch	annels		4 channels	8 channels			
Output	Output Temperature Conversion value		16-bit signed binary(-2000 to 8500: Each increment represents 0.1°C change), 32-bit signed binary(-200000 to 850000: Each increment represents 0.001°C change)	16-bit signed binary (-2000 to 8500: Each increment represents 0.1°C change)			
	Scaling value		16-bit signed binary				
Measuring method (wire type)		od (wire type)	3-/4-wire type	3-wire type			
			Pt100 (JIS C1604-1997, IEC 751 1983) Temperature measurement range: -200 to 850°C				
Usable RTD			JPt100 (JIS C1604-1981) Temperature measurement range: -180 to 600°C				
			Ni100 (DIN43760 1987) Temperature measurement range: -60 to 180°C				
		-200 to 850°C	±0.553°C (25±5°C), ±1.615°C (0 to 55°C)	±0.8°C (25±5°C), ±2.4°C (0 to 55°C)			
	Pt100	-20 to 120°C	±0.090°C (25±5°C), ±0.300°C (0 to 55°C)	±0.3°C (25±5°C), ±1.1°C (0 to 55°C)			
		0 to 200°C	±0.145°C (25±5°C), ±0.470°C (0 to 55°C)	±0.4°C (25±5°C), ±1.2°C (0 to 55°C)			
Accuracy	JPt100	-180 to 600°C	±0.390°C (25±5°C), ±1.140°C (0 to 55°C)	±0.8°C (25±5°C), ±2.4°C (0 to 55°C)			
		-20 to 120°C	±0.090°C (25±5°C), ±0.300°C (0 to 55°C)	±0.3°C (25±5°C), ±1.1°C (0 to 55°C)			
		0 to 200°C	±0.145°C (25±5°C), ±0.470°C (0 to 55°C)	±0.4°C (25±5°C), ±1.2°C (0 to 55°C)			
	Ni100	-60 to 180°C	±0.135°C (25±5°C), ±0.450°C (0 to 55°C)	±0.4°C (25±5°C), ±1.2°C (0 to 55°C)			
Resolutio	n		0.025°C	0.1°C			
Conversi	on spee	d	40 ms/channel	320 ms/8 channels			
No. of an	alog inp	ut points	4 channels/module	8 channels/module			
Isolation	method		Between RTD input and programmable controller power supply: Photocoupler Between RTD input channels: Transformer	Between RTD input and programmable controller power supply: Transformer Between RTD input channels: Transformer			
Disconne	ction de	tection	Yes (each chann	el independently)			
No. of oc	cupied I/	/O points	16 points (I/O assignme	nt: Intelligent 16 points)			
External	connecti	ions	18-point terminal block	40-pin connector			

Temperature control module

Item	Item Q64TCTT		Q64TCRT	Q64TCRTBW		
Control output		Transist	tor output			
No. of temperature input point						
Usable thermocouples/ platinum RTDs	ThermocouplePlatinum RTD(R, K, J, T, S, B, E, N, U, L, PL II, W5Re/W26Re)(Pt100, JPt100)					
Accuracy	Ambient temperature of $25\pm5^{\circ}$ C: Full scale x ($\pm0.3\%$) Ambient temperature of 0 to 55° C: Full scale x ($\pm0.7\%$)					
Sampling cycle	0.5 s/4 channels					
PID constant range		(P): 0.0 to 1000.0% I): 1 to 3600 s (D): 0 to 3600 s				
Isolation method		ground: Transformer shannel: Transformer				
Heater disconnection detection	No	Yes	No	Yes		
No. of occupied I/O points	16 points (I/O assignment: Intelligent 16 points)	32 points/2 slots (Default I/O assignment: Empty 16 points [first half], Intelligent 16 points [second half])	16 points (I/O assignment: Intelligent 16 points)	32 points/2 slots (Default I/O assignment: Empty 16 points [first half], Intelligent 16 points [second half])		
External connections	18-point terminal block	Two 18-point terminal blocks	18-point terminal block	Two 18-point terminal blocks		

Load cell

Item	Q61LD						
Analog input (load cell output) points	1 point (1 channel)						
Analog input (load cell output)	0.0 to 3.3mV/V						
Load cell applied voltage	5VDC ±5%, Output current within 60mA (Four 350Ω load cells can be connected in parallel.) 6-wire system (Combination use of remote sensing method and ratiometric method)						
Digital output	32-bit signed binary 0 to 10000						
Gross weight output (Maximum weighing output value)	32-bit signed binary -99999 to 99999 (excluding decimal point and unit symbol)						
Analog input range (load cell rated output)	0.0 to 1.0mV/V, 0.0 to 2.0mV/V, 0.0 to 3.0mV/V						
I/O characteristics, Maximum resolution	I	Analog in Load cell rated output	put range 0 to 1.0mV/V 0 to 2.0mV/V 0 to 3.0mV/V	Digital output value 0 to 10000	Maximum weighing capacity output value -99999 to 99999	Maximum resolution 0.5 μV 1.0 μV 1.5 μV	
Accuracy Accuracy relative to analog input load cell rated output) of a module)	Nonlineality: Within ±0.01%/FS (Ambient temperature 25°C) Zero drift: Within ±0.25 μV/°C RTI Gain drift: Within ±15 ppm/°C						
Conversion speed					10 ms		
Absolute maximum input					±2.5V		
nsulation method				Photoco	oupler insulation		
No. of occupied I/O points				16 points (I/O assign	nment: intelligent 16 points)		
External connections				18-point	t terminal block		



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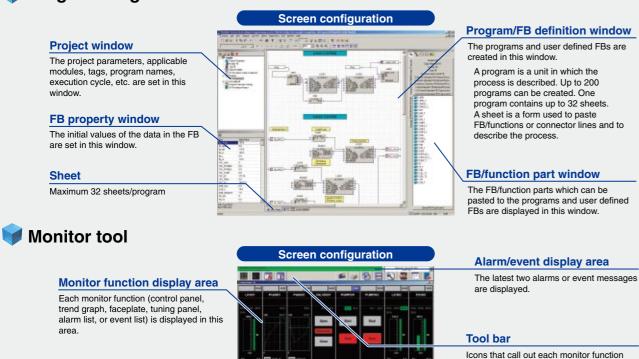
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PX Developer



PX Developer screen configuration and screen examples

🔰 Programming tool



Historical trend graph Event/alarm list FFEE SETE FFEE SEC FRABINET 22 11125955555555551111111111111555 1500001201520111115 1111

are displayed

Process control monitoring screen generator

The PX Developer Monitor tool can generate process control monitoring screens for GOT from process control programs created by the programming tool. Also, graphic screens can be configured by combining the following optional tools with the Monitor tool.



Features

Substantial FBs (function blocks) and functions for loop control

- · In addition to the process CPU's loop control instruction FBs, PX Developer has combined FBs that are easy to use.
- Basic FB/functions (logical operation, arithmetic operation, etc.) that comply with IEC61131-3 are also provided, allowing simple sequence control to be described in the FBD.

Easy programming with FBs/process tag names

- · The loop control program can be created easily. Select the required FBs from the PX Developer's standard loop control FB or compensation FB, paste and connect them on the screen, and then set the parameters such as the PID constants and upper/lower limits (items configuring tags).
- When programming with tag names, the parameters in the tag can be described as "tag name.parameter name" (FIC001.PV, etc.). This enables the user to program without having to concern about the device memory addresses.

Analog/digital I/O processing in FB

FBs for I/O processing of the analog and I/O modules controlled by the process CPU are equipped. Ladder programs for I/O processing are no longer required.

Easy to standardize and reuse programs

· PX Developer complies with the IEC61131-3 standards. Programs can be modularized (custom FB can be created). This allows for reuse in future projects requiring similar capabilities, greatly reducing development time.

Program event execution

FBD programs can be executed not only periodically but also automatically upon event occurrence. (The event conditions can be described without a program.) PX Developer easily realizes the starting process for nonstationary, error, and exceptional processes.

Integration with sequence control programs

EC PROCESS CONTROL

REDUNDANT SYSTEM

Easy data exchange with ladder programs.

· Data can be exchanged between the FBD program and ladder program (created with GX Developer) using logical names (labels) instead of device memory addresses. Therefore, constants of loop control tags, SV values, etc. can be easily changed from the ladder programs.

Uploading/downloading FBD programs

- Graphic data of FBD programs can be stored in the programmable controller CPU by writing.
- The stored data can be recovered by reading from the programmable controller CPU. Therefore, the program can be edited with a PC in which the project is not stored.

Comprehensive tuning and monitoring functions

- · The PX Developer has various screens (face plate, tuning trend, alarm, event list, etc.) used to tune, monitor, and operate the created control loop. Tuning and monitoring are available immediately after creating the program.
- · Auto tuning can be used by the Step Response method or the Limit Cycle method.

Combination with SoftGOT

· Using together with SoftGOT, process control monitoring screens can be readily created. **P.37 P.42**

Improved operability for redundant system

· Users can design the redundant system without repeating the same procedure for the control and standby systems, reducing the total setup and design time.

Supports the server/client monitoring system

- Easy to use by utilizing the Monitor tool.
- · Configuring the server/client monitoring system with lower cost is possible.
- Supports the large process control systems.



PX Developer \mathcal{T}



Specifications

_			
Drog	ramm	nna	tool
FIUG	amm	IIIIM	luui

Item	Specifications			
Target CPU	Process CPU (Q02PHCPU/Q06PHCPU/Q12PHCPU/Q25PHCPU) Redundant CPU (Q12PRHCPU/Q25PRHCPU)			
Target network CC-Link IE Controller Network, MELSECNET/H, MELSECNET/10 (Note 1), Ethernet (10/100 Mbps), RS232 (CPU's RS-232 port), USB (CPU's USB port)				
Programming languages	IEC61131-3 compliant FBD language			
Number of programs	Max. 200 (max. 32 sheets/program)			
Number of tags	Max. 120/process CPU (Q02/06PHCPU) Max. 480/process CPU (Q12/25PHCPU, Q12/25PRHCPU)			
FB/function types	General functions: 58 types (IEC61131-3 compliant basic functions) General FB: 20 types (IEC61131-3 compliant basic FB) Process functions: 5 types (corresponding to process instructions for CPU) General process FB: 28 types (corresponding to process instructions for CPU) Tag access FB: 37 types (corresponding to process instructions for CPU) Tag FB: 46 types (high function FBs with temperature control function by combining process FBs) Module FB: 31 types (FBs for accessing Q Series analog and I/O modules)			
Program execution method	Timer execution type: scan executed at high speed (200 ms cycle), normal speed (200/400/600/800 ms/1 s cycle), or low speed (1/2/4/5/10 s cycle) Interrupt execution type: cycle interrupt (1 to 999 ms) or random interrupt (interrupt with interrupt pointer I0 to I255) (in practical use, 10 ms or more/control loop)			

Note 1) When An, AnS, QnA, QnAS, and Q4AR CPUs are mixed on the network, the target network is MELSECNET/10 PLC to PLC network.

Monitor tool

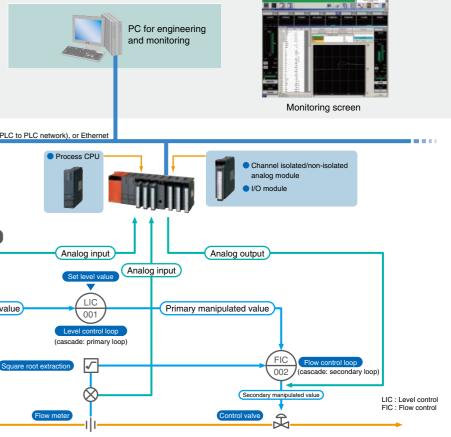
	Item	Specifications				
Tar	get CPU and network	· Same as the programming tool				
	nber of monitoring CPU dules and tags	Number of process CPUs under monitoring: Max. 16 (Max. 16 process CPUs can be monitored from one PC.) Number of monitor tags: Max. 7,680 For the redundant system, a pair of CPUs (control and standby systems) is counted as one CPU.				
	Control panel	8 faceplates/screen (one group) x max. 500 screens = 4,000 faceplates Lockout tag available for each faceplate				
SL	Trend graph	 8 items/screen (one group) x max. 125 screens = 1,000 items Collection cycle: 1 s/10 s/1 min./5 min./10 min. The chart can be output into a CSV file. Automatically generated CSV files can be opened in the trend graph display. 				
Monitor functions	Alarm list	Max. 2,000 alarms can be displayed. The list can be output into a CSV file.				
Monito	Event list	Max. 2,000 events can be displayed. The list can be output into a CSV file.				
	User application	· Max. 4 can be started.				
	Tag data external I/F	· The faceplate can be displayed, monitored, and operated on an external application by using ActiveX faceplate button or faceplate control.				
	GOT screen generator function	Process control monitoring screens for GOT1000 can be automatically generated from process control programs created by the programming tool. (Max.120 tags)				

Application example: Fluid level control

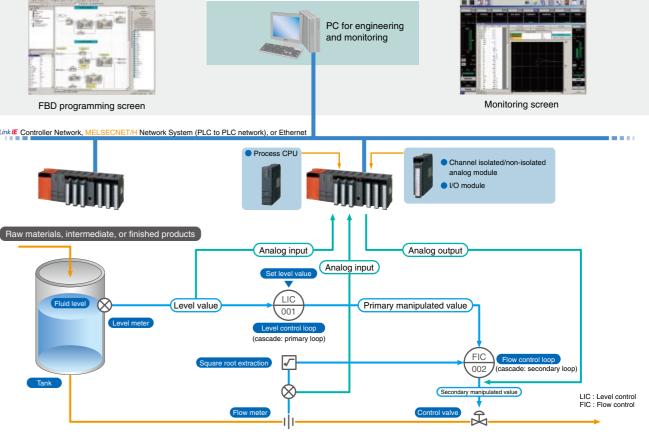
Application

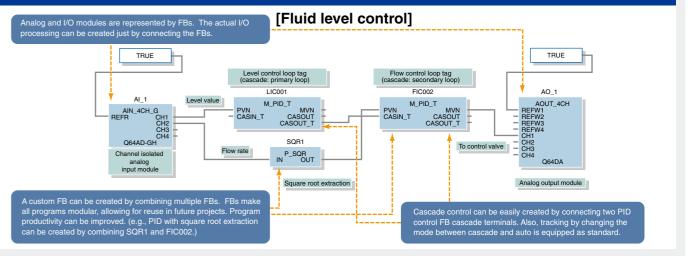
The MELSEC process control system can be used to control fluid level at food and chemical plants. Fluid level of raw materials, intermediate products, and finished products in the tank can be maintained within the set range.











Control details

The level control loop (cascade: primary loop) inputs the tank level (analog value) and implements PI operation to achieve the set level value. The flow control loop (cascade: secondary loop) uses the operation result (primary manipulated value) of the level control loop as the set value and implements PI operation with the flow rate from the flow meter. The result is output as an analog value to the control valve, which is the secondary manipulated value (control valve open).

MELSEC PROCESS CONTROL/

REDUNDANT SYSTEM









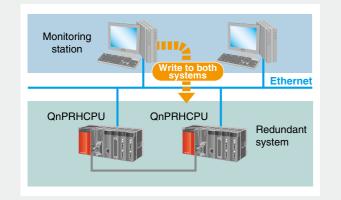
Redundant system related functions (Programming tool)

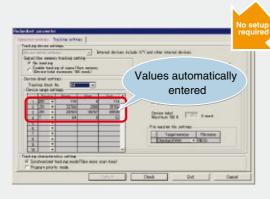
Simple engineering environment

All tag information and programs are managed in a single project; programs and parameters are downloaded to both systems, just like a single system. No special consideration for redundancy is required, reducing engineering time.

No tracking setting required

Device tracking settings in PX Developer are automatically generated by compiling, reducing setup time.





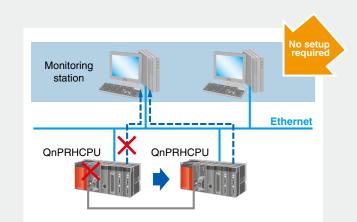
Redundant system related functions (Monitor tool)

Easy to monitor

By setting the control system as the monitoring target in the transfer setup, the new control system is automatically monitored when the system is switched. No extra setting is required for system switching.

Monitoring redundant system communication status Both systems in the redundant system are monitored; the status of

each system (control/standby) and communication errors are displayed.



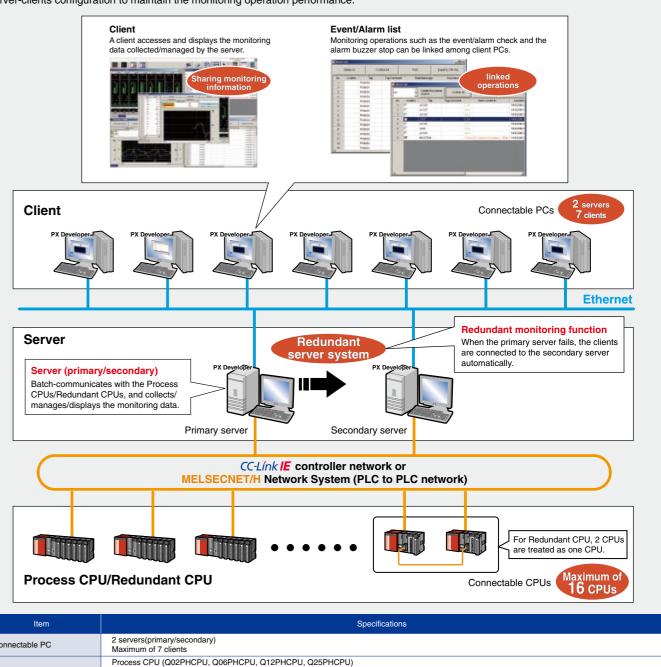
Reject Name		States	Taget 9		tems
ROJECT	Abace and	Nerval Control System	Contral System	Dackup Pitch	
			-	_	

ELSEC PROCESS CONTROL

REDUNDANT SYSTEM

Server/client monitoring system(monitor tool) Supporting large-scaled process control system. Easy and low-cost system configuration. · With the server/client monitoring system, the monitoring information is shared and the monitoring operations are linked. · Even with the expansion of monitoring PCs, the communication load with programmable controllers is controlled by the server-clients configuration to maintain the monitoring operation performance.

Client A client accesses and displays the monitoring data collected/managed by the server.



Item	
Connectable PC	2 servers(primary/secondary) Maximum of 7 clients
Compatible CPU	Process CPU (Q02PHCPU, Q06PHCPU, Q12PHCPU, Q25P Redundant CPU (Q12PRHCPU, Q25PRHCPU) Connectable CPUs:Maximum of 16
Compatible network	CC-Link IE controller network, MELSECNET/H/(10) network
Function overview	 A server performs communication with PLCs in a batch, so of improved. The monitoring data collected/managed by a server can be monitor tool on every OPS. Installing a primary server and a secondary server in the moserver, for clients switch the server to connect automatically When an operation, such as a buzzer stop and check/deletic synchronizes in every OPS in the monitoring system.

system, Ethernet

communication loads on PLCs can be reduced and communication performance can be

e displayed on clients. In other words, the same monitor screen can be displayed with the

nonitoring system enables continuous monitoring when an error occurs in the primary

tion of an alarm/event, is performed in one of OPSs in the monitoring system, its result



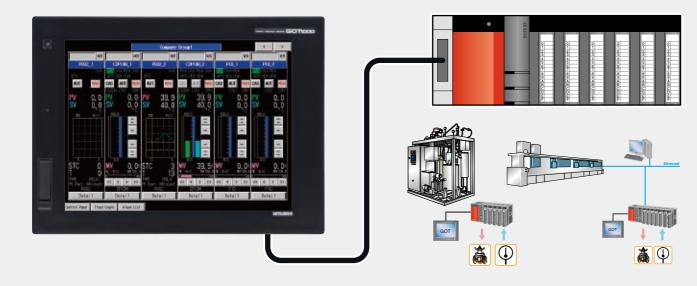
PX Developer \bigcirc



GOT screen generator function

Equipment/shop floor monitoring by GOT1000

GOT1000 can be used for monitoring equipment and shop floor.



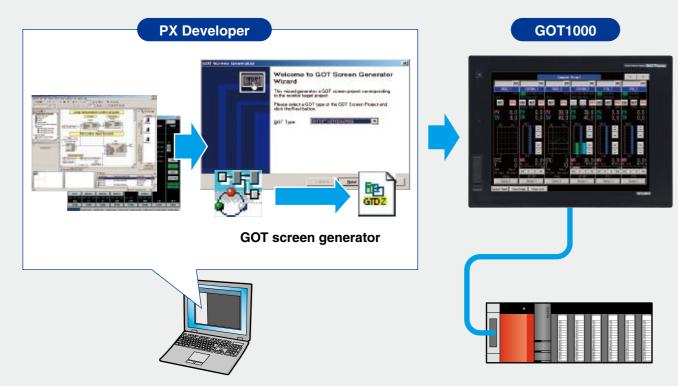
Easy to create GOT1000 process control monitoring screens

· Faceplates and tuning screens for GOT1000 can be automatically generated from PX Developer projects.

· Tag's assigned device settings or programs are not needed for the auto-generated screens.

*Only CPU which is connected to the host station can be monitored.

Multiple CPUs which are connected to other stations cannot be monitored

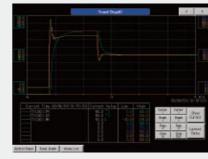


The following screens can be generated with the GOT screen generator function.



Control panel screen AT 0.0 7 29.9 4 4 018 11 11

Trend graph screen



Tag setting screen



Item	
Supported CPU	Process CPU (Q02PHCPU, Q06PHCPU, Q12PHC Redundant CPU (Q12PRHCPU, Q25PRHCPU)
Supported GOT	GT15: Each model of XGA and SVGA GT16: Each model of XGA and SVGA GT SoftGOT1000 (screen size: 1024 x 768 dot [X0
Supported screen design software (Note 1) (Note 2)	GT Designer2 Version 2.82L or later: Q02PHCPU, GT Designer2 Version 2.73B or later: Q12PHCPU
Functions (outline)	Number of generable tags: Max. 120 (loop tags a Generable screens: Faceplate, control panel, and Number of faceplates on control panel: 8/screen Connection path: One-to-one connection betwee Process CPU: CPU direct co Redundant CPU: Computer I Tuning trend cycle: 1 s or more

GT Designer2 is required to use the GOT generator function Note 2) For GT16, changing GOT type with GT Designer2 after generating in GOT15 enables generated screens to be used in GT16.





Tuning screen



Program setting screen



Specifications

HCPU, Q25PHCPU),

XGA], 800 x 600 dot [SVGA] only)

J, Q06PHCPU

U, Q25PHCPU, Q12PRHCPU, Q25PRHCPU

and status tags)

nd detailed screens (tuning screen, setting screen, alarm list screen, and trend graph screen) n (XGA), 6/screen (SVGA)

een GOT1000 and target CPU (host station)

connection, bus connection, computer link connection, Ethernet connection

r link connection, Ethernet connection (Refer to user's manual for details)





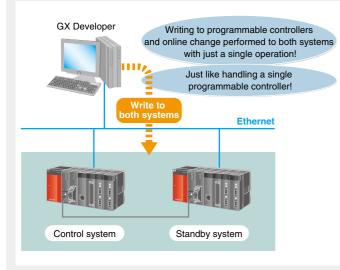
🔰 GX Developer

GX Developer is a comprehensive programming tool that improves work efficiency in development, debugging, and maintenance of programmable controllers. To support the redundant system, it provides dedicated parameter settings and functions such as online program download to both systems (control/standby).

Easy to modify programs

Using the following functions, programs can be written to both control and standby systems simultaneously. These functions simplify program modifications, cutting down development time.

- OWriting programs and parameter files to programmable controllers
- Online change (editing and writing programs to the programmable controllers while online)

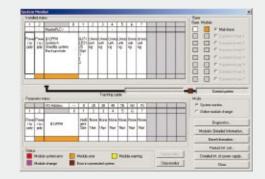


Simple parameter settings for the redundant system The redundant system settings such as tracking settings, which maintain device status of both systems consistent, are designed in a familiar format to GX Developer users.

F 16	anaking	backing participation	5 Name 168. rec	edį	Althocking device capacity 1222 K went
Freed	detail onting long black No o tangar nofit	1	•		F Do suite torward Tracking black No.1 Kato ON SM 15301
	Device	Parts	Stat	End #	E Ports / Start
1	X +	0192	1	1FFF	F Stat/End
2	Y	9792	I	1FFF	14 DIGE / END
2	N .*	3.4	1	2847	Device total
4	28 *	20768	1	32767	Notime 100 K
5					
6		1		2	Fileingster tile ontings
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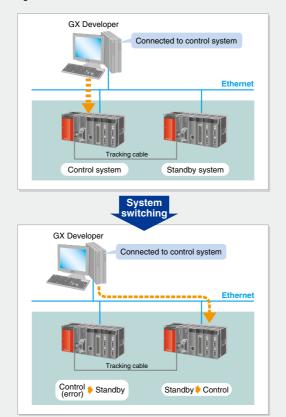
Monitoring module errors

The status of the CPUs, intelligent function modules, and redundant power supply modules can be monitored. Hence, faulty modules can be quickly identified.



Continuous operation even at system switching

In the event of system switching due to a stop error in the CPU, the access target will be automatically switched. The operation continues smoothly, freeing the operator from having to consider system switching.



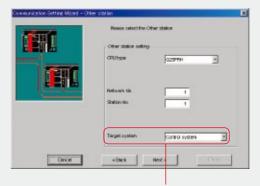
Software supporting redundant system

MX Component

MX Component is an ActiveX control library that supports all communication paths, from the PC to the programmable controller, and enables communications by simple processing. User applications can be easily configured without having to concern about system switching.

Simple communication settings

Selecting "Control system" as the target system makes the redundant system available: all other communication settings are the same as the standard system.



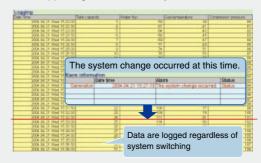
Select "Control system" for the redundant system.

Utilization of existing software

Existing user applications created with MX Component can be used for the redundant system simply by changing the communication settings.

Supports the redundant system

MX Sheet enables monitoring, logging, etc. of the programmable controller system using Excel without programming. It runs by MX Components, supporting the redundant system.



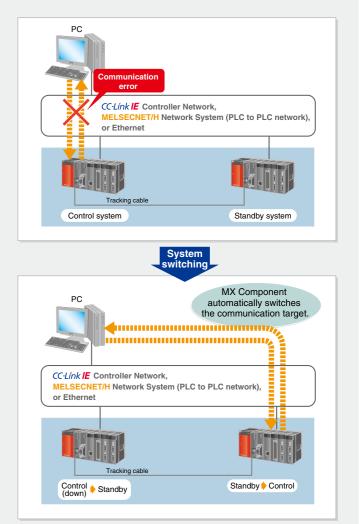
Simplifies troubleshooting after system switching

MX Component constantly monitors some of the redundant CPU devices. Hence, it is easy to know whether the currently-accessed CPU is a control system or standby system. When the system is switched, diagnostics and troubleshooting can be performed using GX Developer.

REDUNDANT SYSTEM

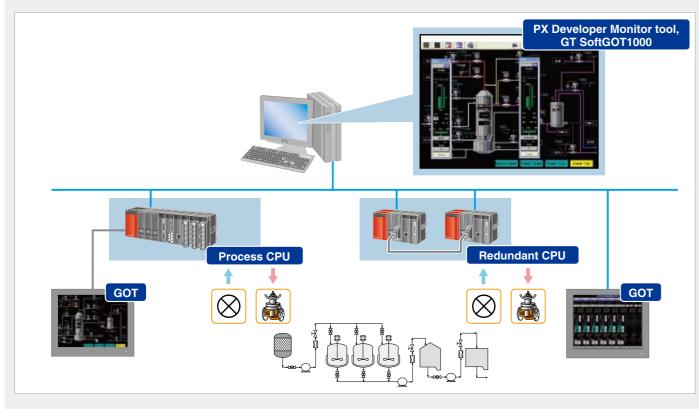
Easy programming

The redundant system application can be programmed without being conscious of the redundancy. When system switching occurs due to a control system failure, communication is automatically continued with the new control system switched from the standby system. Programming is not required to switch the communication target, reducing development time.





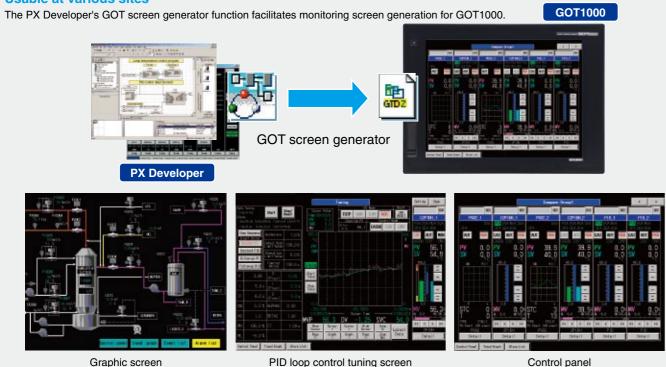
HMI (human machine interface) enables process control system monitoring.



GOT1000

Features





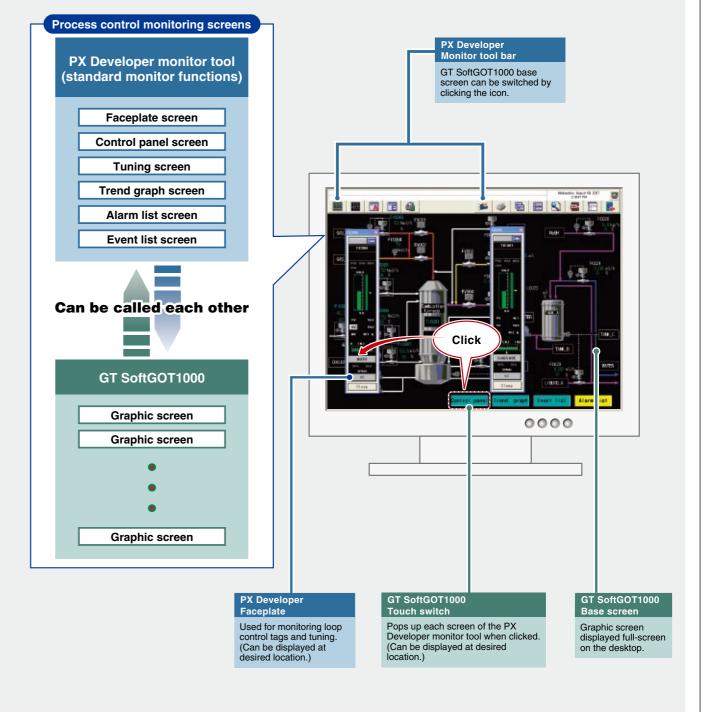
GT SoftGOT1000 \bigcirc

Features

Easy to create process control monitoring screen

- dramatically reducing screen designing time.
- · Can be used on a PC. It is best suited for monitoring at the office.

· GOT1000 screens can be utilized, reducing time for creating new screens. Note 1) PX Developer 1.13P or later is required.



• The standard monitor functions of PX Developer monitor tool can be called from the graphic screen of GT SoftGOT1000 and vice versa^(Note 1),

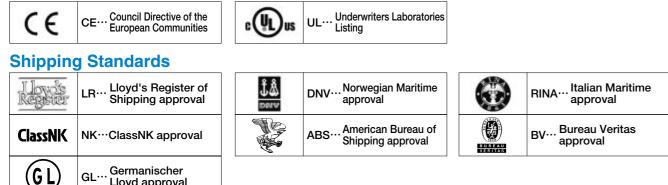
World Wide Support

Ensuring an extensive global support network meeting diverse support for today's needs

Complying with international guality assurance standards

All of Mitsubishi Electric's FA component products have acquired the international guality assurance "ISO9001" and environment management system standard "ISO14001" certification. Mitsubishi's products also comply with various safety standards, including UL standards, and shipping standards.

Safety Standards



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Area covered: China

TIANJIN OFFICE

Area covered: China

Hong Kong

Area covered: China

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REDUNDANT SYSTEM



*Always refer to user's manuals for information on usable modules, restrictions, etc. before using. *Contact your local Mitsubishi sales office or representative for the latest information on the MELSOFT versions and compatible OS.

CPU, power supply

Prod	uct	Model	Outline
		Q00UJCPU	No. of I/O points: 256 points, no. of I/O device points: 8192 points, program capacity: 10 k steps, basic operation processing speed (LD instruction): 0.12 µs, program memory capacity: 40 KB, peripheral connection ports: USB and RS232, no memory card I/F, 5-slot base, with 100 to 240 V AC input / 5 V DC/3 A output power supply
		Q00UCPU	No. of I/O points: 1024 points, no. of I/O device points: 8192 points, program capacity: 10 k steps, basic operation processing speed (LD instruction): 0.08 μs, program memory capacity: 40 KB, peripheral connection ports: USB and RS232, no memory card I/F
		Q01UCPU	No. of I/O points: 1024 points, no. of I/O device points: 8192 points, program capacity: 15 k steps, basic operation processing speed (LD instruction): 0.06 µs, program memory capacity: 60 KB, peripheral connection ports: USB and RS232, no memory card I/F
		Q02UCPU	No. of I/O points: 2048 points, no. of I/O device points: 8192 points, program capacity: 20 k steps, basic operation processing speed (LD instruction): 0.04 μ s, program memory capacity: 80 KB, peripheral connection ports: USB and RS232, with memory card I/F
		Q03UDCPU	No. of I/O points: 4096 points, no. of I/O device points: 8192 points, program capacity: 30 k steps, basic operation processing speed (LD instruction): 0.02 µs, program memory capacity: 120 KB, multiple CPU high-speed communication, peripheral connection ports: USB and RS232, with memory card I/F
Universal model QCPU		Q04UDHCPU	No. of I/O points: 4096 points, no. of I/O device points: 8192 points, program capacity: 40 k steps, basic operation processing speed (LD instruction): 0.0095 μ s, program memory capacity: 160 KB, multiple CPU high-speed communication, peripheral connection ports: USB and RS232, with memory card I/F
		Q06UDHCPU	No. of I/O points: 4096 points, no. of I/O device points: 8192 points, program capacity: 60 k steps, basic operation processing speed (LD instruction): 0.0095 μ s, program memory capacity: 240 KB, multiple CPU high-speed communication, peripheral connection ports: USB and RS232, with memory card I/F
		Q10UDHCPU	No. of I/O points: 4096 points, no. of I/O device points: 8192 points, program capacity: 100 k steps, basic operation processing speed (LD instruction): 0.0095 μ s, program memory capacity: 400 KB, multiple CPU high-speed communication, peripheral connection ports: USB and RS232, with memory card I/F
		Q13UDHCPU	No. of I/O points: 4096 points, no. of I/O device points: 8192 points, program capacity: 130 k steps, basic operation processing speed (LD instruction): 0.0095 μ s, program memory capacity: 520 KB, multiple CPU high-speed communication, peripheral connection ports: USB and RS232, with memory card I/F
		Q20UDHCPU	No. of I/O points: 4096 points, no. of I/O device points: 8192 points, program capacity: 200 k steps, basic operation processing speed (LD instruction): 0.0095 μ s, program memory capacity: 800 KB, multiple CPU high-speed communication, peripheral connection ports: USB and RS232, with memory card I/F
		Q26UDHCPU	No. of I/O points: 4096 points, no. of I/O device points: 8192 points, program capacity: 260 k steps, basic operation processing speed (LD instruction): 0.0095 μ s, program memory capacity: 1040 KB, multiple CPU high-speed communication, peripheral connection ports: USB and RS232, with memory card I/F
		Q03UDECPU	No. of I/O points: 4096 points, no. of I/O device points: 8192 points, program capacity: 30 k steps, basic operation processing speed (LD instruction): 0.02 μs, program memory capacity: 120 KB, multiple CPU high-speed communication, peripheral connection ports: USB and Ethernet, with memory card I/F
		Q04UDEHCPU	No. of I/O points: 4096 points, no. of I/O device points: 8192 points, program capacity: 40 k steps, basic operation processing speed (LD instruction): 0.0095 μ s, program memory capacity: 160 KB, multiple CPU high-speed communication, peripheral connection ports: USB and Ethernet, with memory card I/F
		Q06UDEHCPU	No. of I/O points: 4096 points, no. of I/O device points: 8192 points, program capacity: 60 k steps, basic operation processing speed (LD instruction): 0.0095 μ s, program memory capacity: 240 KB, multiple CPU high-speed communication, peripheral connection ports: USB and Ethernet, with memory card I/F
		Q10UDEHCPU	No. of I/O points: 4096 points, no. of I/O device points: 8192 points, program capacity: 100 k steps, basic operation processing speed (LD instruction): 0.0095 μ s, program memory capacity: 400 KB, multiple CPU high-speed communication, peripheral connection ports: USB and Ethernet, with memory card I/F
	Built-in Ethernet type	Q13UDEHCPU	No. of I/O points: 4096 points, no. of I/O device points: 8192 points, program capacity: 130 k steps, basic operation processing speed (LD instruction): 0.0095 μ s, program memory capacity: 520 KB, multiple CPU high-speed communication, peripheral connection ports: USB and Ethernet, with memory card I/F
		Q20UDEHCPU	No. of I/O points: 4096 points, no. of I/O device points: 8192 points, program capacity: 200 k steps, basic operation processing speed (LD instruction): 0.0095 μ s, program memory capacity: 800 KB, multiple CPU high-speed communication, peripheral connection ports: USB and Ethernet, with memory card I/F
		Q26UDEHCPU	No. of I/O points: 4096 points, no. of I/O device points: 8192 points, program capacity: 260 k steps, basic operation processing speed (LD instruction): 0.0095 μ s, program memory capacity: 1040 KB, multiple CPU high-speed communication, peripheral connection ports: USB and Ethernet, with memory card I/F
		Q50UDEHCPU NEW	No. of I/O points: 4096 points, no. of I/O device points: 8192 points, program capacity: 500 k steps, basic operation processing speed (LD instruction): 0.0095 μ s, program memory capacity: 2000 KB, multiple CPU high-speed communication, peripheral connection ports: USB and Ethernet, with memory card I/F
		Q100UDEHCPU	No. of I/O points: 4096 points, no. of I/O device points: 8192 points, program capacity: 1000 k steps, basic operation processing speed (LD instruction): 0.0095 μ s, program memory capacity: 4000 KB, multiple CPU high-speed communication, peripheral connection ports: USB and Ethernet, with memory card I/F
		Q00JCPU	No. of I/O points: 256 points, no. of I/O device points: 2048 points, program capacity: 8 k steps, basic operation processing speed (LD instruction): 0.2 μs, program memory capacity: 58 KB, peripheral connection ports: RS232, no memory card I/F, 5-slot base, with 100 to 240 V AC input / 5 V DC/3 A output power supply
Basic model QCPU		Q00CPU	No. of I/O points: 1024 points, no. of I/O device points: 2048 points, program capacity: 8 k steps, basic operation processing speed (LD instruction): 0.16 μs, program memory capacity: 94 KB, peripheral connection ports: RS232, no memory card I/F
		Q01CPU	No. of I/O points: 1024 points, no. of I/O device points: 2048 points, program capacity: 14 k steps, basic operation processing speed (LD instruction): 0.1 μs, program memory capacity: 94 KB, peripheral connection ports: RS232, no memory card I/F

CPU, power supply

/			
Pro	duct	Model	
		Q02CPU	No. of I/O points: 4096 points, no. of basic operation processing speed (LD peripheral connection ports: RS232, w
		Q02HCPU	No. of I/O points: 4096 points, no. of basic operation processing speed (LD peripheral connection ports: USB and
High Performant	ce model QCPU	Q06HCPU	No. of I/O points: 4096 points, no. of basic operation processing speed (LD peripheral connection ports: USB and
		Q12HCPU	No. of I/O points: 4096 points, no. of basic operation processing speed (LD peripheral connection ports: USB and
		Q25HCPU	No. of I/O points: 4096 points, no. of basic operation processing speed (LD peripheral connection ports: USB and
		Q02PHCPU	No. of I/O points: 4096 points, no. of basic operation processing speed (LD peripheral connection ports: USB and
		Q06PHCPU	No. of I/O points: 4096 points, no. of basic operation processing speed (LD peripheral connection ports: USB and
Process CPU		Q12PHCPU	No. of I/O points: 4096 points, no. of basic operation processing speed (LD
		Q25PHCPU	peripheral connection ports: USB and No. of I/O points: 4096 points, no. of basic operation processing speed (LD
		Q12PRHCPU	peripheral connection ports: USB and No. of I/O points: 4096 points, no. of basic operation processing speed (LD
Redundant CP	U	Q25PRHCPU	peripheral connection ports: USB and No. of I/O points: 4096 points, no. of basic operation processing speed (LD
		QC10TR	peripheral connection ports: USB and Tracking cable 1 m
	Tracking cable	QC30TR	Tracking cable 3 m
		Q12DCCPU-V	No. of I/O points: 4096 points, endian
C Controller C	PU	Q06CCPU-V	No. of I/O points: 4096 points, endian
		Q12DCCPU-CB (Note 1)	RS-232 connection converter cable (c
		GT05-MEM-128MC (Note 2)	128 MB CompactFlash card
		GT05-MEM-256MC (Note 2)	256 MB CompactFlash card
	Option	QD81MEM-512MBC	512 MB CompactFlash card
		QD81MEM-1GBC	1 GB CompactFlash card
		QD81MEM-2GBC (Note 1)	2 GB CompactFlash card
		QD81MEM-4GBC (Note 1)	4 GB CompactFlash card
		QD81MEM-8GBC (Note 1)	8 GB CompactFlash card
		Q6BAT	Replacement battery
		Q7BAT	Replacement large-capacity battery
Battery		Q7BAT-SET	Large-capacity battery with holder for
		Q8BAT	Replacement large-capacity battery me
		Q8BAT-SET	Large-capacity battery module with CF
		Q2MEM-1MBS	SRAM memory card, capacity: 1 MB
		Q2MEM-2MBS	SRAM memory card, capacity: 2 MB
	Q3MEM-4MBS		SRAM memory card, capacity: 4 MB
		Q3MEM-4MBS-SET	SRAM memory card with cover, capac
Moment		Q3MEM-8MBS	SRAM memory card, capacity: 8 MB
Memory card		Q3MEM-8MBS-SET	SRAM memory card with cover, capac
		Q2MEM-2MBF Q2MEM-4MBF	Linear Flash memory card, capacity: 2
		Q2MEM-4MBF	Linear Flash memory card, capacity: 4 ATA card, capacity: 8 MB
		Q2MEM-16MBA	ATA card, capacity: 16 MB
Memory card	adanter	Q2MEM-32MBA	ATA card, capacity: 32 MB Adapter for O2MEM memory card's st
Memory card a	adapiel	Q2MEM-ADP Q2MEM-BAT	Adapter for Q2MEM memory card's sta Replacement battery for Q2MEM-1MB
SRAM card ba	attery	Q2MEM-BAT	Replacement battery for Q2MEM-1ME Replacement battery for Q3MEM-4ME
Connection ca	ble	QC30R2	RS-232 cable for connecting personal
Cable disconn			
prevention hole		Q6HLD-R2	Holder for preventing RS-232 cable (p

Note 1) For use with Q12DCCPU-V Note 2) For use with Q06CCPU-V

Outline

of I/O device points: 8192 points, program capacity: 28 k steps, D instruction): 0.079 μs , program memory capacity: 112 KB, with memory card I/F f I/O device points: 8192 points, program capacity: 28 k steps, D instruction): 0.034µs, program memory capacity: 112 KB, B RS232, with memory card I/F f I/O device points: 8192 points, program capacity: 60 k steps, I instruction): 0.034µs, program memory capacity: 240 KB, I RS232, with memory card I/F of I/O device points: 8192 points, program capacity: 124 k steps, D instruction): 0.034μ s, program memory capacity: 496 KB, RS232, with memory card I/F f I/O device points: 8192 points, program capacity: 252 k steps, D instruction): 0.034 μ s, program memory capacity: 1008 KB, d RS232, with memory card I/F f I/O device points: 8192 points, program capacity: 28 k steps, D instruction): 0.034 μ s, program memory capacity: 112 KB, d RS232, with memory card I/F of I/O device points: 8192 points, program capacity: 60 k steps, D instruction): 0.034 μ s, program memory capacity: 240 KB, d RS232, with memory card I/F f I/O device points: 8192 points, program capacity: 124 k steps, D instruction): 0.034µs, program memory capacity: 496 KB, B RS232, with memory card I/F of I/O device points: 8192 points, program capacity: 252 k steps, D instruction): 0.034 μ s, program memory capacity: 1008 KB, RS232, with memory card I/F of I/O device points: 8192 points, program capacity: 124 k steps, D instruction): 0.034 μ s, program memory capacity: 496 KB, d RS232, with memory card I/F f I/O device points: 8192 points, program capacity: 252 k steps, D instruction): 0.034 μ s, program memory capacity: 1008 KB, d RS232, with memory card I/F n format: little endian, removable storage: CF card, OS:VxWorks Version 6.4 n format: little endian, removable storage: CF card, OS:VxWorks Version 5.4 custom mini-DIN to 9-pin D-sub connector)

ELSEC PROCESS CONTROL

REDUNDANT SYSTEM

r mounting CPU module

CPU connection cable

acity: 4 MB

acity: 8 MB 2 MB 4 MB

standard PCMCIA slot IBS and Q2MEM-2MBS IBS and Q3MEM-8MBS al computer and CPU, 3 m (between mini-DIN6P and Dsub9P)

(programmable controller CPU connection) disconnection



Rase

Product	Model	Outline		
	Q33B	3 slots, 1 power supply module required, for Q Series modules		
Main hann	Q35B	5 slots, 1 power supply module required, for Q Series modules		
Main base	Q38B	8 slots, 1 power supply module required, for Q Series modules		
	Q312B	12 slots, 1 power supply module required, for Q Series modules		
Multiple CPU high speed	Q38DB	8 slots, 1 power supply module required, for Q Series modules		
main base	Q312DB	12 slots, 1 power supply module required, for Q Series modules		
	Q32SB	2 slots, 1 slim type power supply module required, for Q Series modules		
Slim type main base	Q33SB	3 slots, 1 slim type power supply module required, for Q Series modules		
	Q35SB	5 slots, 1 slim type power supply module required, for Q Series modules		
Redundant power main base	Q38RB	8 slots, 2 redundant power supply modules required, for Q Series modules		
	Q63B	3 slots, 1 power supply module required, for Q Series modules		
	Q65B	5 slots, 1 power supply module required, for Q Series modules		
Eutopoion booo	Q68B	8 slots, 1 power supply module required, for Q Series modules		
Extension base	Q612B	12 slots, 1 power supply module required, for Q Series modules		
	Q52B	2 slots, power supply module not required, for Q Series modules		
	Q55B	5 slots, power supply module not required, for Q Series modules		
Redundant power extension base Q68RB 8 slots, 2 redundant power supply m		8 slots, 2 redundant power supply modules required, for Q Series modules		
Redundant type extension base (Note 1) 5 slots, 2 redundant power supply modules required, for Q Series modules		5 slots, 2 redundant power supply modules required, for Q Series modules		
	QC05B	0.45 m cable for connecting extension base unit		
	QC06B	0.6 m cable for connecting extension base unit		
Extension cable	QC12B	1.2 m cable for connecting extension base unit		
Extension cable	QC30B	3 m cable for connecting extension base unit		
	QC50B	5 m cable for connecting extension base unit		
	QC100B	10 m cable for connecting extension base unit		
	Q6DIN1	DIN rail mounting adapter for Q38B, Q312B, Q68B, Q612B, Q38RB, Q68RB, Q65WRB, Q38DB, and Q312DB		
DIN roll mounting adoptor	Q6DIN2	DIN rail mounting adapter for Q35B, Q65B, and Q00UJCPU		
DIN rail mounting adapter	Q6DIN3	DIN rail mounting adapter for Q32SB, Q33SB, Q35SB, Q33B, Q52B, Q55B, and Q63B		
	Q6DIN1A	DIN rail mounting adapter (with vibration-proofing bracket set) for Q3_B, Q5_B, Q6_B, Q38RB, Q68RB, and Q65WR		
Blank cover	QG60	Blank cover for I/Q slot		

Note 1) Only compatible with redundant CPU system.

Power supply

	Q61P	Input voltage: 100 to 240 V AC, output voltage: 5 V DC, output current: 6 A
	Q62P	Input voltage: 100 to 240 V AC, output voltage: 5/24 V DC, output current: 3/0.6 A
Power supply	Q63P	Input voltage: 24 V DC, output voltage: 5 V DC, output current: 6 A
	Q64PN	Input voltage: 100 to 240 V AC, output voltage: 5 V DC, output current: 8.5 A
Power Supply with Life Q61P-D Slim type power supply Q61SP		Input voltage: 100 to 240 V AC, output voltage: 5 V DC, output current: 6A
		Input voltage: 100 to 240 V AC, output voltage: 5 V DC, output current: 2 A
Redundant power supply	Q63RP	Input voltage: 24 V DC, output voltage: 5 V DC, output current: 8.5 A
	Q64RP	Input voltage: 100 to 120/200 to 240 V AC, output voltage: 5 V DC, output current: 8.5 A

I/O module

Proc	duct	Model		
		QX10		16 points, 100 to 120 V AC, response
	AC	QX10-TS		16 points, 100 to 120 V AC, response
		QX28		8 points, 100 to 240 V AC, response
		QX40		16 points, 24 V DC, response time: 1
		QX40-TS		16 points, 24 V DC, response time: 1, 18-point spring clamp terminal block
		QX40-S1		16 points, 24 V DC, response time: 0.
	DC	QX40H		16 points, 24 V DC, response time: 0,
	(Positive	QX41 (Note 2	?) (Note 4)	32 points, 24 V DC, response time: 1
	common) (Note 1)	QX41-S1	(Note 2)	32 points, 24 V DC, response time: 0.
		QX41-S2 NEW (Note 2	!) (Note 4)	32 points, 24 V DC, response time: 1/
		QX42	(Note 2)	64 points, 24 V DC, response time: 1,
		QX42-S1	(Note 2)	64 points, 24 V DC, response time: 0.
Input	AC/DC	QX50		16 points, 48 V AC/DC, response tim
		QX70		16 points, 5/12 V DC, response time: 18-point terminal block
	DC sensor	QX70H		16 points, 5 V DC, response time: 0/0
		QX71	(Note 2)	32 points, 5/12 V DC, response time:
		QX72	(Note 2)	64 points, 5/12 V DC, response time:
		QX80		16 points, 24 V DC, response time: 1
		QX80-TS		16 points, 24 V DC, response time: 1, 18-point spring clamp terminal block
		QX80H		16 points, 24 V DC, response time: 0,
	DC (Negative	4.101	3) (Note 4)	32 points, 24 V DC, response time: 1
	common) (Note 1)	QX81-S2 NEW (Note 3	l) (Note 4)	32 points, 24 V DC, response time: 1/
		QX82	(Note 2)	64 points, 24 V DC, response time: 1
		QX82-S1	(Note 2)	64 points, 24 V DC, response time: 0.
		QX90H		16 points, 5 V DC, response time: 0/0
		QY10		16 points, 24 V DC/240 V AC, 2 A/p
	Relay	QY10-TS		16 points, 24 V DC/240 V AC, 2 A/p 18-point spring clamp terminal block
		QY18A		8 points, 24 V DC/240 V AC, 2 A/po
	Triac	QY22		16 points, 100 to 240 V AC, 0.6 A/po 18-point terminal block, with surge sup
		QY40P		16 points, 12 to 24 V DC, 0.1 A/poin 18-point terminal block, with overload
		QY40P-TS		16 points, 12 to 24 V DC, 0.1 A/point 18-point spring clamp terminal block, v
	Transistor (Sink)	QY41P	(Note 2)	32 points, 12 to 24 V DC, 0.1 A/poin 40-pin connector, with overload protect
		QY42P	(Note 2)	64 points, 12 to 24 V DC, 0.1 A/poin 40-pin connector, with overload protect
Output		QY50		16 points, 12 to 24 V DC, 0.5 A/poin 18-point terminal block, with surge sur
	Transistor (Independent)	QY68A		8 points, 5 to 24 V DC, 2 A/point, 8 A 18-point terminal block, with surge sur
	TTL CMOS	QY70		16 points, 5 to 12 V DC, 16 mA/point 18-point terminal block, with fuse
		QY71 (Note 2)		32 points, 5 to 12 V DC, 16 mA/point 40-pin connector, with fuse
		QY80		16 points, 12 to 24 V DC, 0.5 A/poin 18-point terminal block, with surge sur
	Transistor	QY80-TS		16 points, 12 to 24 V DC, 0.5 A/poin 18-point spring clamp terminal block,
	(Source)	QY81P (Note 3)		32 points, 12 to 24 V DC, 0.1 A/poin 37-pin D-sub connector, with overload
		QY82P NEW	(Note 2)	64 points, 12 to 24 V DC, 0.1 A/poin 40-pin connector, with overload protect
lote 1) "Positive	common" indic:	ates that the nositi	ive lea	d of a DC power supply must be conne

Note 1) "Positive common" indicates that the positive lead of a DC power supply must be connected to the common terminal. Accordingly, "Negative common" indicates that the negative lead must be connected to the common terminal.

Note 2) Connector is not provided. Separately order one of the following: A6CON1/A6CON2/A6CON3/A6CON4. Note 3) Connector is not provided. Separately order one of the following: A6CON1E/A6CON2E/A6CON3E. Note 4) The rated input currents are different. [QX41: approx. 4 mA, QX41-S2: approx. 6 mA, QX81: approx. 4 mA, QX81-S2: approx. 6 mA]

se time: 20 ms, 16 points/common, 18-point terminal block se time: 20 ms, 16 points/common, 18-point spring clamp terminal block e time: 20 ms, 8 points/common, 18-point terminal block

1/5/10/20/70 ms, 16 points/common, positive common, 18-point terminal block 1/5/10/20/70 ms, 16 points/common, positive common,

C PROCESS CONTROL

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0.1/0.2/0.4/0.6/1 ms, 16 points/common, positive common, 18-point terminal block 0/0.1/0.2/0.4/0.6/1 ms, 8 points/common, positive common, 18-point terminal block 1/5/10/20/70 ms, 32 points/common, positive common, 40-pin connector 0.1/0.2/0.4/0.6/1 ms, 32 points/common, positive common, 40-pin connector

1/5/10/20/70 ms, 32 points/common, positive common, 40-pin connector

1/5/10/20/70 ms, 32 points/common, positive common, 40-pin connector 0.1/0.2/0.4/0.6/1 ms, 32 points/common, positive common, 40-pin connector me: 20 ms, 16 points/common, positive/negative common, 18-point terminal block e: 1/5/10/20/70 ms, 16 points/common, positive/negative common,

/0.1/0.2/0.4/0.6/1 ms, 8 points/common, positive common, 18-point terminal block e: 1/5/10/20/70 ms, 32 points/common, positive/negative common, 40-pin connector e: 1/5/10/20/70 ms, 32 points/common, positive/negative common, 40-pin connector 1/5/10/20/70 ms, 16 points/common, negative common, 18-point terminal block 1/5/10/20/70 ms, 16 points/common, negative common,

0/0.1/0.2/0.4/0.6/1 ms, 8 points/common, negative common, 18-point terminal block 1/5/10/20/70 ms. 32 points/common, negative common, 37-pin D-sub connector

1/5/10/20/70 ms, 32 points/common, negative common, 37-pin D-sub connector

1/5/10/20/70 ms, 32 points/common, negative common, 40-pin connector 0.1/0.2/0.4/0.6/1 ms, 32 points/common, negative common, 40-pin connector /0.1/0.2/0.4/0.6/1 ms, 8 points/common, negative common, 18-point terminal block /point, 8 A/common, response time: 12 ms, 16 points/common, 18-point terminal block /point, 8 A/common, response time: 12 ms, 16 points/common,

point, response time: 12 ms, 18-point terminal block, all points independent point, 4.8 A/common, response time: 1 ms + 0.5 cycle, 16 points/common, uppressor

int, 1.6 A/common, response time: 1 ms, 16 points/common, sink type, d protection function and overheat protection function and surge suppressor int, 1.6 A/common, response time: 1 ms, 16 points/common, sink type, , with overload protection function and overheat protection function and surge suppressor

int, 2 A/common, response time: 1 ms, 32 points/common, sink type, ection function and overheat protection function and surge suppressor

nt, 2 A/common, response time: 1 ms, 32 points/common, sink type, ection function and overheat protection function and surge suppressor

int, 4 A/common, response time: 1 ms, 16 points/common, sink type,

uppressor and fuse

A/module, response time: 10 ms, sink/source type, uppressor, all points independent

int, 256 mA/common, response time: 0.5 ms, 16 points/common, sink type.

int, 512 mA/common, response time: 0.5 ms, 32 points/common, sink type,

int, 4 A/common, response time: 1 ms, 16 points/common, source type, suppressor and fuse

nt, 4 A/common, response time: 1 ms, 16 points/common, source type, with surge suppressor and fuse

nt, 2 A/common, response time: 1 ms, 32 points/common, source type,

ad protection function and overheat protection function and surge suppressor

int, 2 A/common, response time: 1 ms, 32 points/common, source type,

ection function and overheat protection function and surge suppressor



I/O module

Product		Model	Outline	
		QH42P (Note 1) (Note 2)	Input: 32 points, 24 V DC, response time: 1/5/10/20/70 ms, 32 points/common, positive common, output: 32 points, 12 to 24 V DC, 0.1 A/point, 2 A/common, response time: 1 ms, 32 points/common, sink type, 40-pin connector, with overload protection function and overheat protection function and surge suppressor	
I/O tra	DC input/ transistor output	QX48Y57	Input: 8 points, 24 V DC, response time: 1/5/10/20/70 ms, 8 points/common, positive common, output: 7 points, 12 to 24 V DC, 0.5 A/point, 2 A/common, response time: 1 ms, 7 points/common, sink type, 18-point terminal block, with surge suppressor and fuse	
		QX41Y41P (Note 1) (Note 2)	Input: 32 points, 24 V DC, response time: 1/5/10/20/70 ms, 32 points/common, positive common, output: 32 points, 12 to 24 V DC, 0.1 A/point, 2 A/common, response time: 1 ms, 32 points/common, sink type, 40-pin connector, with overload protection function and overheat protection function and surge suppressor	
Interrupt module		Q160	16 point, 24 V DC, response time: 0.1/0.2/0.4/0.6/1 ms, 16 points/common, 18-point terminal block	
		A6CON1	32-point connector soldering type (40-pin connector)	
		A6CON2	32-point connector crimp-contact type (40-pin connector)	
		A6CON3	32-point connector pressure-displacement (flat cable) type (40-pin connector)	
Connector		A6CON4	32-point connector soldering type (40-pin connector, cable connectable in bidirection)	
		A6CON1E	32-point connector soldering type (37-pin D-sub connector)	
		A6CON2E	32-point connector crimp-contact type (37-pin D-sub connector)	
		A6CON3E	32-point connector pressure-displacement (flat cable) type (37-pin D-sub connector)	
Spring clamp tern	ninal block	Q6TE-18S	For 16-point I/O modules, 0.3 to 1.5 mm ² (22 to 16 AWG)	
Terminal black or	lontor	Q6TA32	For 32-point I/O modules, 0.5 mm ² (20 AWG)	
Terminal block ac	Japter	Q6TA32-TOL	Q6TA32 dedicated tool	
		A6TBXY36	For positive common input modules and sink output modules (standard type)	
		A6TBXY54	For positive common input modules and sink output modules (2-wire type)	
		A6TBX70	For positive common input modules (3-wire type)	
Connector/termin		A6TBX36-E	For negative common input modules (standard type)	
conversion modul		A6TBX54-E	For negative common input modules (2-wire type)	
		A6TBX70-E	For negative common input modules (3-wire type)	
		A6TBY36-E	For source output modules (standard type)	
		A6TBY54-E	For source output modules (2-wire type)	
		AC05TB	For A6TBXY36, A6TBXY54, and A6TBX70 (positive common/sink type), 0.5 m	
		AC10TB	For A6TBXY36, A6TBXY54, and A6TBX70 (positive common/sink type), 1 m	
		AC20TB	For A6TBXY36, A6TBXY54, and A6TBX70 (positive common/sink type), 2 m	
		AC30TB	For A6TBXY36, A6TBXY54, and A6TBX70 (positive common/sink type), 3 m	
		AC50TB	For A6TBXY36, A6TBXY54, and A6TBX70 (positive common/sink type), 5 m	
	abla	AC80TB	For A6TBXY36, A6TBXY54, and A6TBX70 (positive common/sink type), 8 m *Common current 0.5 A or lower	
	able	AC100TB	For A6TBXY36, A6TBXY54, and A6TBX70 (positive common/sink type), 10 m *Common current 0.5 A or lower	
		AC05TB-E	For A6TBX36-E, A6TBY36-E, A6TBX54-E, A6TBY54-E, and A6TBX70-E (negative common/source type), 0.5 m	
		AC10TB-E	For A6TBX36-E, A6TBY36-E, A6TBX54-E, A6TBY54-E, and A6TBX70-E (negative common/source type), 1 m	
		AC20TB-E	For A6TBX36-E, A6TBY36-E, A6TBX54-E, A6TBY54-E, and A6TBX70-E (negative common/source type), 2 m	
		AC30TB-E	For A6TBX36-E, A6TBY36-E, A6TBX54-E, A6TBY54-E, and A6TBX70-E (negative common/source type), 3 m	
		AC50TB-E	For A6TBX36-E, A6TBY36-E, A6TBX54-E, A6TBY54-E, and A6TBX70-E (negative common/source type), 5 m	
Relay terminal mo	odule	A6TE2-16SRN	For 40-pin connector 24 V DC transistor output modules (sink type)	
		AC06TE	For A6TE2-16SRN, 0.6 m	
	Cable	AC10TE	For A6TE2-16SRN, 1 m	
Ca		AC30TE	For A6TE2-16SRN, 3 m	
		AC50TE	For A6TE2-16SRN, 5 m	
		AC100TE	For A6TE2-16SRN, 10 m	

Note 1) A connector is not provided. Order one of the following separately: A6CON1/A6CON2/A6CON3/A6CON4. Note 2) The number of occupied input/output points is different. [QH42P: 32 points; QX41Y41P: 64 points (first 32 points: input / second 32 points: output)]

Analog I/O module

Pro	duct	Model	
	Voltage input	Q68ADV	8 channels, input: -10 to 10 V DC, o 0 to 16000, -16000 to 16000, conv
		Q62AD-DGH	2 channels; input, 4 to 20 mA DC, or 18-point terminal block, channel isola
	Current input	Q66AD-DG (Note 1)	6 channels, input: 4 to 20 mA DC (w output (resolution): 0 to 4000, 0 to 1 supplies power to 2-wire transmitter
Analog input		Q68ADI	8 channels, input: 0 to 20 mA DC, or 0 to 16000, -16000 to 16000, conv
		Q64AD	4 channels; input -10 to 10 V DC, 0 -12000 to 12000, 0 to 16000, -160
	Voltage/ current input	Q64AD-GH	4 channels, input: -10 to 10 V DC, 0 -64000 to 64000, conversion speed:
		Q68AD-G (Note 1)	8 channels, input: -10 to 10 V DC, 0 -12000 to 12000, 0 to 16000, -160
	Voltage output	Q68DAVN	8 channels, input (resolution): 0 to 40 output: -10 to 10 V DC, conversion transformer isolation between power
	Current output	Q68DAIN	8 channels, input (resolution): 0 to 40 conversion speed: 80 μ s/channel, 18
Analog		Q62DAN	2 channels, input (resolution): 0 to 40 output: -10 to 10 V DC, 0 to 20 mA transformer isolation between power
output	Voltage/ current output	Q62DA-FG	2 channels, input (resolution): 0 to 12 output: -12 to 12 V DC, 0 to 22 mA channel isolated
		Q64DAN	4 channels, input (resolution): 0 to 40 output: -10 to 10 V DC, 0 to 20 mA transformer isolation between power
		Q66DA-G (Note 1)	6 channels, input (resolution): 0 to 40 output: -12 to 12 V DC, 0 to 22 mA
Analog input/ output	Voltage and current input/ output	Q64AD2DA NEW	Input: 4 channels Input: -10 to 10 V Output (resolution): 0 to 4000, -4 Conversion speed: 500 µs/char Output: 2 channels Input (resolution) Output: -10 to 10 V DC, 0 to 20 Conversion speed: 500 µs/char 18-point terminal block
Load cell input	t	Q61LD NEW	1 channel, input (load cell output): 0.0 to
		Q64RD	4 channels, platinum RTD (Pt100 [Jl conversion speed: 40 ms/channel, 1
	RTD	Q64RD-G	4 channels, RTD (Pt100 [JIS C1604 conversion speed: 40 ms/channel, 14
_		Q68RD3-G (Note 1)	8 channels, RTD (3-wire type, Pt100 Ni100 Ω [DIN43760 1987]), conver
Temperature input		Q64TD	4 channels, thermocouple (JIS C160
		Q64TDV-GH	4 channels, thermocouple (JIS C160 sampling cycle: 20 ms/channel, 18-p
	Thermocouple	Q68TD-G-H01 (Note 1) (Note 2	
		Q68TD-G-H02 (Note 1)	8 channels, thermocouple (JIS C160 conversion speed: 640 ms/8 channe
	Platinum RTD	Q64TCRT	4 channels, platimum RTD (Pt100, J 18-point terminal block
Temperature		Q64TCRTBW	4 channels, platimum RTD (Pt100, J two 18-point terminal blocks
control	Thermocouple	Q64TCTT	4 channels, thermocouple (K, J, T, B, sampling cycle: 0.5 s/4 channels, 18
		Q64TCTTBW	4 channels, thermocouple (K, J, T, B, sampling cycle: 0.5 s/4 channels, tw
Loop control		Q62HLC	2 channels, input: thermocouple/mici sampling cycle: 25 ms/2 channels; o 18-point terminal block, with 5 PID c

Note 1) A connector is not provided. The A6CON4 connector must be ordered separately. Note 2) The number of modules that can be installed is restricted based on the combination of power supply and base unit.

Outline
output (resolution): 0 to 4000, -4000 to 4000, 0 to 12000, -12000 to 12000, version speed: 80 µs/channel, 18-point terminal block
butput (resolution): 0 to 32000, 0 to 64000, conversion speed: 10 ms/2 channels, lated, supplies power to 2-wire transmitter
when 2-wire transmitter is connected), 0 to 20 mA DC, 12000, conversion speed: 10 ms/channel, 40-pin connector, channel isolated,
putput (resolution): 0 to 4000, -4000 to 4000, 0 to 12000, -12000 to 12000, version speed: 80 µs/channel, 18-point terminal block
0 to 20 mA DC, output (resolution): 0 to 4000, -4000 to 4000, 0 to 12000, 000 to 16000, conversion speed: 80 µs/channel, 18-point terminal block
0 to 20 mA DC, output (resolution): 0 to 32000, -32000 to 32000, 0 to 64000, d: 10 ms/4 channels, 18-point terminal block, channel isolated
0 to 20 mA DC, output (resolution): 0 to 4000, -4000 to 4000, 0 to 12000, 000 to 16000, conversion speed: 10 ms/channel, 40-pin connector, channel isolated
4000, -4000 to 4000, 0 to 12000, -12000 to 12000, -16000 to 16000, a speed: 80 μ s/channel, 18-point terminal block, r supply and output
0000, -4000 to 4000, 0 to 12000, -12000 to 12000; output: 0 to 20 mA DC, 8-point terminal block, transformer isolation between power supply and output
4000, -4000 to 4000, 0 to 12000, -12000 to 12000, -16000 to 16000, DC, conversion speed: 80 μs/channel, 18-point terminal block, r supply and output
2000, -12000 to 12000, -16000 to 16000, DC, conversion speed: 10 ms/2 channels, 18-point terminal block,
4000, -4000 to 4000, 0 to 12000, -12000 to 12000, -16000 to 16000, DC, conversion speed: 80 μs/channel, 18-point terminal block, r supply and output
4000, -4000 to 4000, 0 to 12000, -12000 to 12000, -16000 to 16000, NDC, conversion speed: 6 ms/channel, 40-pin connector, channel isolated
/ DC, 0 to 20 mA DC 4000 to 4000, 0 to 12000, -12000 to 12000, 0 to 16000, -16000 to 16000 Innel
n): 0 to 4000,-4000 to 4000,0 to 12000,-16000 to 16000 10 mA DC Innel
o 3.3 mV/V, output (resolution): 0 to 10000, conversion speed: 10 ms, 18-point terminal block
JIS C1604-1997, IEC 751 1983], JPt100 [JIS C1604-1981]), 18-point terminal block
4-1997, IEC 751 1983], JPt100 [JIS C1604-1981], Ni100 Ω [DIN43760 1987]), 18-point terminal block, channel isolated
0 [JIS C1604-1997, IEC 751 1983], JPt100 [JIS C1604-1981]), rsion speed: 320 ms/8 channels, 40-pin connector, channel isolated
02-1995), conversion speed: 40 ms/channel, 18-point terminal block
02-1995), micro voltage (-100 to 100 mV), conversion speed: sampling cycle x 3, -point terminal block
02-1995, IEC 60584-1 [1995], IEC 60584-2 [1982]), els, 40-pin connector
02-1995, IEC 60584-1 [1995], IEC 60584-2 [1982]), els, 40-pin connector
JPt100), no heater disconnection detection, sampling cycle: 0.5 s/4 channels,
JPt100), with heater disconnection detection, sampling cycle: 0.5 s/4 channels,
 S, E, R, N, U, L, PLII, W5Re/W26Re), no heater disconnection detection, 8-point terminal block

ELSEC PROCESS CONTROL/ **REDUNDANT SYSTEM**

B, S, E, R, N, U, L, PLII, W5Re/W26Re), with heater disconnection detection, two 18-point terminal blocks

icro voltage/voltage/current, conversion speed (input): 25 ms/2 channels, ; output: 4 to 20 mA DC, conversion speed (output): 25 ms/2 channels; control modes



Pulse I/O and positioning module

Product		Model		Outline	
Channel isolated pulse input		QD60P8-G		8 channels, 30 kpps/10 kpps/1 kpps/100 pps/50 pps/10 pps/1 pps/0.1 pps, count input signal: 5/12 to 24 V DC	
		QD62	(Note 1)	2 channels, 200/100/10 kpps, count input signal: 5/12/24 V DC, external input: 5/12/24 V DC, coincidence output: transistor (sink), 12/24 V DC, 0.5 A/point, 2 A/common, 40-pin connector	
			(Note 1)	2 channels, 500/200/100/10 kpps, count input signal: EIA standards RS-422-A (differential line driver), external input: 5/12/24 V DC; coincidence output: transistor (sink), 12/24 V DC, 0.5 A/point, 2 A/common, 40-pin connector	
High-speed co	punter	QD62E	(Note 1)	2 channels, 200/100/10 kpps, count input signal: 5/12/24 V DC, external input: 5/12/24 V DC, coincidence output: transistor (source), 12/24 V DC, 0.1 A/point, 0.4 A/common, 40-pin connector	
		QD63P6	(Note 2)	6 channels, 200/100/10 kpps, count input signal: 5 V DC, 40-pin connector	
		QD64D2 NEW	(Note 2)	2 channels, 4 Mpps, count input signal: EIA standards RS-422-A (differential line driver), external input: 24 V DC, coincidence output: transistor (sink), 12/24 V DC, 0.5 A/point, 2 A/common, 40-pin connector	
		QD75P1	(Note 2)	1 axis, control unit: mm, inch, degree, pulse, no. of positioning data: 600/axis, max. output pulse: 200 kpps, 40-pin connector	
	Open collector	QD75P2	(Note 2)	2 axes, 2-axis linear interpolation, 2-axis circular interpolation, control unit: mm, inch, degree, pulse, no. of positioning data: 600/axis, max. output pulse: 200 kpps, 40-pin connector	
	output	QD75P4	(Note 2)	4 axes, 2-/3-/4-axis linear interpolation, 2-axis circular interpolation, control unit: mm, inch, degree, pulse, no. of positioning data: 600/axis, max. output pulse: 200 kpps, 40-pin connector	
		QD70P4	(Note 2)	4 axes, control unit: pulse, no. of positioning data: 10/axis, max. output pulse: 200 kpps, 40-pin connector	
		QD70P8	(Note 2)	8 axes, control unit: pulse, no. of positioning data: 10/axis, max. output pulse: 200 kpps, 40-pin connector	
	Differential output	QD75D1	(Note 2)	1 axis, control unit: mm, inch, degree, pulse, no. of positioning data: 600/axis, max. output pulse: 1 Mpps, 40-pin connector	
		QD75D2	(Note 2)	2 axes, 2-axis linear interpolation, 2-axis circular interpolation, control unit: mm, inch, degree, pulse, no. of positioning data: 600/axis, max. output pulse: 1 Mpps, 40-pin connector	
		QD75D4	(Note 2)	4 axes, 2-/3-/4-axis linear interpolation, 2-axis circular interpolation, control unit: mm, inch, degree, pulse, no. of positioning data: 600/axis, max. output pulse: 1 Mpps, 40-pin connector	
		QD70D4	(Note 2)	4 axes, control unit: pulse, no. of positioning data: 10/axis, max. output pulse: 4 Mpps, 40-pin connector	
		QD70D8	(Note 2)	8 axes, control unit: pulse, no. of positioning data: 10/axis, max. output pulse: 4 Mpps, 40-pin connector	
Positioning		QD75M1	(Note 1)	1 axis, control unit: mm, inch, degree, pulse, no. of positioning data: 600/axis, 40-pin connector	
	With SSCNET connectivity	QD75M2	(Note 1)	2 axes, 2-axis linear interpolation, 2-axis circular interpolation, control unit: mm, inch, degree, pulse, no. of positioning data: 600/axis, 40-pin connector	
		QD75M4	(Note 1)	4 axes, 2-/3-/4-axis linear interpolation, 2-axis circular interpolation, control unit: mm, inch, degree, pulse, no. of positioning data: 600/axis, 40-pin connector	
		QD75MH1	(Note 1)	1 axis, control unit: mm, inch, degree, pulse, no. of positioning data: 600/axis, 40-pin connector, with SSCNET III connectivity	
	With SSCNET	QD75MH2	(Note 1)	2 axes, 2-axis linear interpolation, 2-axis circular interpolation, control unit: mm, inch, degree, pulse, no. of positioning data: 600/axis, 40-pin connector, with SSCNET III connectivity	
		QD75MH4	(Note 1)	4 axes, 2-/3-/4-axis linear interpolation, 2-axis circular interpolation, control unit: mm, inch, degree, pulse, no. of positioning data: 600/axis, 40-pin connector, with SSCNET II connectivity	
		QD74MH8		8 axes, control unit: pulse, no. of positioning data: 32/axis, with SSCNET III connectivity	
		QD74MH16		16 axes, control unit: pulse, no. of positioning data: 32/axis, with SSCNET II connectivity	
	Open collector output with built-in counter function	QD72P3C3	(Note 2)	Positioning: 3 axes, control unit: pulse, no. of positioning data: 1/axis, max. output pulse: 100 kpps, counter: 3 channels, 100 kpps, count input signal: 5/24 V DC, 40-pin connector	

Note 1) A connector is not provided. The A6CON1/A6CON2/A6CON3/A6CON4 connector must be ordered separately. Note 2) A connector is not provided. The A6CON1/A6CON2/A6CON4 connector must be ordered separately.

Information module

Proc	duct	Model	
MES interface		QJ71MES96	MES interface module *MX MESInter
		GT05-MEM-128MC	128 MB CompactFlash card
	Option	GT05-MEM-256MC	256 MB CompactFlash card
	Option	QD81MEM-512MBC	512 MB CompactFlash card
		QD81MEM-1GBC	1 GB CompactFlash card
High-Speed Da	ata Logger	QD81DL96	High-Speed Data Logger module *Co
		QD81MEM-512MBC	512 MB CompactFlash card
	Option	QD81MEM-1GBC	1 GB CompactFlash card
		QD81MEM-2GBC	2 GB CompactFlash card
		QD81MEM-4GBC	4 GB CompactFlash card
		QD81MEM-8GBC	8 GB CompactFlash card
		QJ71E71-100	10BASE-T/100BASE-TX
Ethernet		QJ71E71-B2	10BASE2
		QJ71E71-B5	10BASE5
		QJ71C24N	RS-232: 1 channel, RS-422/485: 1 c
Serial communication		QJ71C24N-R2	RS-232: 2 channels, total transmissio
		QJ71C24N-R4	RS-422/485: 2 channels, total transn
		QD51	BASIC program execution module, RS
Intelligent com	munication	QD51-R24	BASIC program execution module, RS
Intelligent communication		SW1IVD-AD51HP	Software package for QD51, AD51H-

Note 1) The program is run in Windows[®] command prompt.

Control network module

CC-Link IE Controller		QJ71GP21-SX	Multi-mode fiber optic cable, dual loop, controller network (control/normal station)
Network		QJ71GP21S-SX	Multi-mode fiber optic cable, dual loop, controller network (control/normal station), with external power supply function
		QJ71LP21-25	SI/QSI/H-PCF/ broadband H-PCF fiber optic cable, dual loop, controller network (control/normal station) or remote I/O network (remote mater station)
	Optical loop (SI)	QJ71LP21S-25	SI/QSI/H-PCF/ broadband H-PCF fiber optic cable, dual loop, controller network (control/normal station) or remote I/O network (remote mater station), with external power supply function
		QJ72LP25-25	SI/QSI/H-PCF/ broadband H-PCF fiber optic cable, dual loop, remote I/O network (remote I/O station)
MELSECNET/H	Optical	QJ71LP21G	GI-50/125 fiber optic cable, dual loop, controller network (control/normal station) or remote I/O network (remote master station)
	loop (GI)	QJ72LP25G	GI-50/125 fiber optic cable, dual loop, remote I/O network (remote I/O station)
	Coaxial bus	QJ71BR11	3C-2V/5C-2V coaxial cable, single bus, controller network (control/normal station) or remote I/O network (remote master station)
		QJ72BR15	3C-2V/5C-2V coaxial cable, single bus, remote I/O network (remote I/O station)
	Twist bus	QJ71NT11B	Twisted pair cable, single bus, controller network (control/normal station)
CC-Link IE Field Network		QJ71GF11-T2 NEW (Note 1)	Master/local station, CC-Link IE Field Network compatible
CC-Link		QJ61BT11N	Master/local station, CC-Link Ver. 2 compatible
CC-Link/LT		QJ61CL12	Master station
		QJ71FL71-T-F01	10BASE-T, 100BASE-TX
	Ver. 2.00	QJ71FL71-B2-F01	10BASE2
FL-net (OPCN-2)		QJ71FL71-B5-F01	10BASE5
	Ver. 1.00	QJ71FL71-T	10BASE-T
		QJ71FL71-B2	10BASE2
		QJ71FL71-B5	10BASE5
AS-i QJ71AS92		QJ71AS92	Master station, AS-Interface Specification Version 2.11 compatible

Note 1) Supported by Universal model CPUs whose (first five serial number digits are 12012 or later). GX Works2 version 1.25B or later is required for configuration and diagnosis functions.

Outline
erface and CompactFlash card are required.
CompactFlash card are required
channel, total transmission speed of 2 channels: 230.4 kbps
ion speed of 2 channels: 230.4 kbps
smission speed of 2 channels: 230.4 kbps
RS-232: 2 channels
RS-232: 1 channel, RS-422/485: 1 channel
H-S3, and A1SD51S



Replacement support MELSEC-A/QnA transition products

Product		Model		Outline
	Main base	Q35BL	(Note 1)	5 slots. Power supply module installation required. For Q Series large input/output module installation
	Ividii i base	Q38BL	(Note 1)	8 slots. Power supply module installation required. For Q Series large input/output module installation
		Q65BL	(Note 1)	5 slots. Power supply module installation required. For Q Series large input/output module installation
Q Large base	Extension base	Q68BL	(Note 1)	8 slots. Power supply module installation required. For Q Series large input/output module installation
	babb	Q55BL	(Note 1)	5 slots. Power supply module installation not required. For Q Series large input/output module installation
	Large blank cover	QG69L	(Note 1)	For gap adjustment when a previous Q Series module is installed on the Q large base
		QX11L	(Note 1)	For replacement of "A" large module "AX11". 32 points, 100 to 120 V AC, response time: 25 ms, 32 points/common, 38-point terminal block
	Input	QX21L NEW	(Note 1)	For replacement of "A" large module "AX21". 32 points, 200 to 240 V AC, response time: 25 ms, 32 points/common, 38-point terminal block
Q Large I/O	Output	QY11AL NEW	(Note 1)	For replacement of "A" large module "AY10A, AY11A". 16 points, 24 V DC/240 V AC, 2 A/point; 16 A/all points, all-point independent contacts, response time: 12 ms, 38-point terminal block
		QY13L	(Note 1)	For replacement of "A" large module "AY13". 32 points, 24 V DC/240 V AC, 2 A/point; 5 A/common, 8 points/common, response time: 12 ms, 38-point terminal block
		QY23L	(Note 1)	For replacement of "A" large module "AY23". 32 points, 100 to 240 V AC; 0.6 A/point, 2.4 A/common, 8 points/common, response time: 1 ms + 0.5 cycle, 38-point terminal block
		QD62-H01	(Note 2)	For replacement of "A" large module "AD61". 2 channels, 50 kpps, count input signal: 5/12/24 V DC, external input: 5/12/24 V DC, coincidence output: transistor (sync), 12/24 V DC, 0.5 A/point; 2 A/common
High Speed C	ounter	QD62-H02	(Note 2)	For replacement of "A" large module "AD61-S1". 2 channels, 10 kpps, count input signal: 5/12/24 V DC, external input: 5/12/24 V DC, coincidence output: transistor (sync), 12/24 V DC, 0.5 A/point; 2 A/common
	Are Constitute	QA1S65B	(Note 3)	5 slots. Requires AnS series power supply module installation. For AnS series module installation
Extension	AnS series	QA1S68B	(Note 3)	8 slots. Requires AnS series power supply module installation. For AnS series module installation
base	Accesion	QA65B	(Note 3)	5 slots. Requires A series power supply module installation. For A series module installation
	A series	QA68B	(Note 3)	8 slots. Requires A series power supply module installation. For A series module installation
For MELSECN	IET (II) local	A1SJ71AP23Q	(Note 3)	Optic cable, duplex loop, MELSECNET (II) local station
station	. ,	A1SJ71AR23Q	(Note 3)	3C-2V/5C-2V coaxial cable, duplex loop, MELSECNET (II) local station
For MELSECN station	For MELSECNET/B local station		(Note 3)	Twisted pair cable, single bus, MELSECNET/B local station
	البيط بالمتعالم فاست	L'als Deufeureres		and Universal OCDLI (Evoluting OO0LICDLI)

Note 1) Only supported only by High Performance QCPU and Universal QCPU (Excluding Q00UJCPU). Note 2) A connector is not provided. Please order one of the following separately: A6CON1/A6CON2/A6CON3/A6CON4 Note 3) Only supported only by High Performance model QCPU.

PC interface board

	Proc	duct	Model	
	CC-Link IE controller network		Q80BD-J71GP21-SX	PCI bus/PCI-X bus, Japanese/Englis controller network (control/normal sta
		Itroller network	Q80BD-J71GP21S-SX	PCI bus/PCI-X bus, Japanese/Englis controller network (control/normal sta
			Q81BD-J71LP21-25	PCI Express bus, Japanese/English controller network (control/normal sta
		Optical loop (SI) Optical loop (GI)	Q80BD-J71LP21-25	PCI bus, Japanese/English OS comp controller network (control/normal sta
	MELSECNET/H (10)		Q80BD-J71LP21S-25	PCI bus, Japanese/English OS comp controller network (control/normal sta
			Q80BD-J71LP21G	PCI bus, Japanese/English OS comp controller network (control/normal sta
		Coaxial bus	Q80BD-J71BR11	PCI bus, Japanese/English OS comp controller network (control/normal sta
	CC-Link		Q81BD-J61BT11	PCI Express bus, Japanese/English
			Q80BD-J61BT11N	PCI bus, Japanese/English OS comp

ELSEC PROCESS CONTROL/ REDUNDANT SYSTEM

utline

lish OS compatible, multi-mode fiber optic cable, dual loop, station)

glish OS compatible, multi-mode fiber optic cable, dual loop, station), with external power supply function

h OS compatible, SI/QSI/H-PCF/broadband H-PCF fiber optic cable, dual loop,

station)

npatible, SI/QSI/H-PCF/broadband H-PCF fiber optic cable, dual loop, station)

mpatible, SI/QSI/H-PCF/broadband H-PCF fiber optic cable, dual loop, station), with external power supply function

mpatible, GI-50/125 fiber optic cable, dual loop, station)

npatible, 3C-2V/5C-2V coaxial cable, single bus, station)

h OS compatible, master/local interface board, CC-Link Ver. 2 compatible npatible, master/local interface board, CC-Link Ver. 2 compatible



*Contact your local Mitsubishi sales office or representative for the latest information about software versions and compatibility.

			Compatible CPU						
Product	Model	Model Outline		al model	High Performance model	Basic model	Process CPU	Redund CPU	
GX Works2 🕕	SW1DNC-GXW2-E	PLC engineering software (software with integrated functions including tools for programming, simulation and various module setting/monitoring)	•	•	•	•	_	—	
	SW8D5C-GPPW-E	MELSEC programmable controller programming software	•	(Note 1)	•	•	•	•	
GX Developer 🕕	SW8D5C-GPPW-EV	MELSEC programmable controller programming software (upgrade)	•	(Note 1)	•	•	•	•	
GX Simulator (Note 2)	SW7D5C-LLT-E	MELSEC programmable controller simulation software	•	(Note 1)	•	•	•	•	
GX Simulator (1) (Note 2)	SW7D5C-LLT-EV	MELSEC programmable controller simulation software (upgrade)	•	(Note 1)	•	•	•	•	
GX Converter () (Note 2)	SW0D5C-CNVW-E	Excel/text data converter	—	-	•	•	•	•	
GX Configurator-AD	SW2D5C-QADU-E	MELSEC-Q dedicated analog to digital conversion module setting/ monitoring tool	•	(Note 1)	•	•	•	•	
GX Configurator-DA	SW2D5C-QDAU-E	MELSEC-Q dedicated digital to analog conversion module setting/ monitoring tool	•	(Note 1)	•	•	•	•	
GX Configurator-SC (Note 2)	SW2D5C-QSCU-E	MELSEC-Q dedicated serial communication module setting/monitoring tool	•	(Note 1)	•	•	•	•	
GX Configurator-CT (Note 2)	SW0D5C-QCTU-E	MELSEC-Q dedicated high-speed counter module setting/monitoring tool	•	(Note 1)	•	•	•	•	
GX Configurator-TC (Note 2)	SW0D5C-QTCU-E	MELSEC-Q dedicated temperature control module setting/monitoring tool	•	(Note 1)	•	•	•	•	
GX Configurator-TI	SW1D5C-QTIU-E	MELSEC-Q dedicated temperature input module setting/monitoring tool	•	(Note 1)	•	•	•	•	
GX Configurator-FL	SW0D5C-QFLU-E	MELSEC-Q dedicated FL-net module setting/monitoring tool	•	(Note 1)	•	•	•	•	
GX Configurator-PT	SW1D5C-QPTU-E	MELSEC-Q dedicated positioning module QD70 setting/monitoring tool	•	(Note 1)	•	•	•	•	
GX Configurator-AS	SW1D5C-QASU-E	MELSEC-Q dedicated AS-i master module setting/monitoring tool	•	(Note 1)	•	•	•	•	
GX Configurator-QP 🕕	SW2D5C-QD75P-E	MELSEC-Q dedicated positioning module QD75P/D/M setting/monitoring tool	•	(Note 1)	•	•	•	•	
GX Explorer 🔶	SW2D5C-EXP-E	Maintenance tool	-	-	•	•	(Note 3)	-	
GX RemoteService-I 🔶	SW2D5C-RAS-E	Remote access tool	_	-	•	•	(Note 3)	-	
GX Works	SW4D5C-QSET-E	A set of seven products: GX Developer, GX Simulator, GX Explorer, GX Configurator-AD, DA, SC, CT			(Not	te 4)			
	SW8D5C-GPPLLT-E	A set of three products: GX Developer, GX Simulator, GX Explorer			(Not	te 4)			

Note 1) Not compatible with Q50UDEHCPU, Q100UDEHCPU, and QJ71GF11-T2. Note 2) Operates at GX Developer add-in software. GX Developer is required. Note 3) Not compatible with Q02PHCPU and Q06PHCPU. Note 4) For details regarding set-item compatible CPUs, refer to the compatible CPUs for the single products.

MELSOFT PX Series

				Compatible CPU						
Product	Model	Outline	Univers	al model	High Performance	Basic		Redundant		
			QnU	QnUD(E)	model	model	CPU	CPU		
PX Developer 🛈	SW1D5C-FBDQ-E	Process control FBD software package	—	-	-	-	•	•		
PX Works	SW3D5C-FBDGPP-E	A set of six products: PX Developer, GX Developer, GX Configurator-AD, DA, CT, TI			(No	ite)				
Note) For details regarding	set-item compatible CPU	s, refer to the compatible CPUs for the single products.								

MELSOFT MX Series

MX Component 🔶	SW3D5C-ACT-E	ActiveX library for communication	• • • • •		•	
MX Sheet 🔶	SW1D5C-SHEET-E	Excel® communication support tool	• • • • • •		•	
MX Works	SW1D5C-SHEETSET-E	A set of two products: MX Component, MX Sheet	(Note 1)			
MX MESInterface	SW1DNC-MESIF-E	MES interface module QJ71MES96 dedicated information linkage tool	(Note 2)			
Note 1) For details regarding set-item compatible CPUs, refer to the compatible CPUs for the single products. Note 2) Required when using the MES interface module.						

MELSOFT iQ Works

		FA engineering software (Note)				
	SW1DNC-IQWK-E NEW (CD edition)	System management software "MELSOF Upstream design, tool for linkage to iQ				
MELSOFT iQ Works 🔶		PLC engineering software "MELSOFT G2 Tools for PLC programming, simulation				
	SW1DND-IQWK-E	Motion controller engineering software "N Total support tools for motion controller				
	(DVD edition)	Display screen creation software "MELS Support tools for display screen creation				
late) For details concerning compatible models for each activers item, refer to the relevant pro						

Note) For details concerning compatible models for each software item, refer to the relevant product manuals.

0	Multiple licenses, additional licenses, and multiple license upgrades are available
•	Multiple licenses and additional licenses are available.
¢	Multiple licenses are available.

* For details regarding compatible CPU types, refer to the "Compatible CPUs" item.

OFT Navigator" iQ Works products

GX Works2" tion and various module setting/monitoring

"MELSOFT MT Works2"

ller design and maintenance

SOFT GT Works3" tion

Compatible CPUs

Item		Model			
	QnU	Q00UJ, Q00U, Q01U, Q02U			
Universal model QCPU	QnUD(E)	Q03UD(E), Q04UD(E)H, Q06UD(E)H, Q10UD(E)H, Q13UD(E)H, Q20UD(E)H, Q26UD(E)H, Q50UDEH, Q100UDEH			
High Performance model QCPU		Q02, Q02H, Q06H, Q12H, Q25H			
Basic model QCPU		Q00J, Q00, Q01			
Process CPU		Q02PH, Q06PH, Q12PH, Q25PH			
Redundant CPU		Q12PRH, Q25PRH			





MELSEC PROCESS CONTROL/ REDUNDANT SYSTEM

_____ _____ _____ _____ _____ _____ _____ _____ _____ _____ Mitsubishi Electric Corporation Nagoya Works is a factory certified for ISO14001 (standards for environmental management systems) and ISO9001 (standards for quality assurance management systems) UKAS ISO 9001 BUREAU VERITAS Certification MELSOFT

Mitsubishi Programmable Controllers

Precautions for Choosing the Products

This publication explains the typical features and functions of the Q Series programmable controllers and does not provide restrictions and other information on usage and module combinations. When using the products, always read the user's manuals of the products.

Mitsubishi will not be held liable for damage caused by factors found not to be the cause of Mitsubishi; opportunity loss or lost profits caused by faults in the Mitsubishi products; damage, secondary damage, accident compensation caused by special factors unpredictable by Mitsubishi; damages to products other than Mitsubishi products; and to other duties.

🚹 For safe use

 To use the products given in this publication properly, always read the "manuals" before starting to use them.

- The products have been manufactured as general-purpose parts for general industries, and have not been designed or manufactured to be incorporated in a device or system used in purposes related to human life.
- Before using the products for special purposes such as nuclear power, electric power, aerospace, medicine or passenger movement vehicles, consult with Mitsubishi.
- The products have been manufactured under strict quality control. However, when installing the products where major accidents or losses could occur if the products fail, install appropriate backup or failsafe functions in the system.

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